

# Heterogeneously Integrated Optoelectronic Devices Enabled by Micro-Transfer Printing

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Transfer printing is a materials assembly technique that uses elastomeric stamps for heterogeneous integration of various classes of micro- and nano-structured materials into two- and three-dimensionally organized layouts on virtually any type of substrate. Work over the past decade demonstrates that the capabilities of this approach create opportunities for a wide range of device platforms, including component- and system-level embodiments in unusual optoelectronic technologies with characteristics that cannot be replicated easily using conventional manufacturing or growth techniques. This review presents recent progress in functional materials and advanced transfer printing methods, with a focus on active components that emit, absorb, and/or transport light, ranging from solar cells to light-emitting diodes, lasers, photodetectors, and integrated collections of these in functional systems, where the key ideas provide unique solutions that address limitations in performance and/or functionality associated with traditional technologies. High-concentration photovoltaic modules based on multijunction, micro- and millimeter-scale solar cells and high-resolution emissive displays based on microscale inorganic light-emitting diodes provide examples of some of the most sophisticated systems, geared toward commercialization.

simple yet remarkably versatile scheme for manipulating material elements with dimensions ranging from nanometers to centimeters and arranging them into functional devices and systems.<sup>[6–8]</sup> Here, a soft stamp serves as a tool for removing such elements from a source substrate and delivering them to a target surface, in a massively parallel fashion under ambient conditions, thereby allowing the co-integration of dissimilar materials: single-crystalline materials with various lattice constants and noncrystalline materials; those with high and low thermal stability; those with high and low moduli, and so on. Many of the most prominent examples involve transfer printing with semiconducting, device-grade, inorganic materials such as silicon nanomembranes or III–V platelets to form 2D and 3D assemblies on foreign substrates such as plastic foils or rubber slabs in flexible and stretchable electronics. The results provide high-performance characteristics from materials optimized for their respective roles, inde-

pendent of concerns of epitaxy, growth conditions, thermal budgets, solvent compatibility, or others.

This review highlights recent advances in heterogeneously integrated optoelectronic devices enabled by transfer printing, ranging from solar cells to light-emitting diodes (LEDs), lasers, photodetectors, and digital imaging systems. Detailed descriptions of methods of transfer printing and their application in other technologies can be found elsewhere.<sup>[6,7,9,10]</sup> In all cases, transfer printing bypasses many key challenges in traditional processing and fabrication approaches, thereby creating new functionalities and/or enhancing the performance and cost-effectiveness of existing ones. Emerging commercial

## 1. Introduction

Many advances in science and technology follow from capabilities for combining dissimilar materials in unconventional formats. The resulting heterogeneously integrated systems will feature prominently in future technological developments, where cost-competitive, scalable strategies allow integration of diverse materials with complementary performance attributes in ways that do not limit their intrinsic properties.<sup>[1–5]</sup> Transfer printing, an emerging, high-speed method for materials assembly, provides unique features in this context; it represents an enabling manufacturing technique that offers a

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applications in photovoltaics and emissive display systems represent some of the most advanced examples.

## 2. Transfer Printing

Transfer printing involves the use of a soft elastomer with engineered features of relief on its surface (i.e., a soft stamp) for high speed, precision, nondestructive retrieval of spatially selected collections of micro- and nanostructured materials (i.e., solid 'inks') formed on a substrate (i.e., 'donor' substrate) for the purpose of delivery and distribution into two- and three-dimensionally organized arrays on a substrate of interest (i.e., 'receiver' substrate) in a massively parallel, deterministic manner (Figure 1a).<sup>[6–8]</sup> The 'ink' elements can be fabricated, for example, from high-quality bulk silicon, silicon-on-insulator (SOI), or epitaxially grown III–V compound semiconductors in schemes that involve anisotropic or selective wet chemical etching of underlying 'sacrificial' materials to release them from donor substrates. For the preparation of ink materials in printable forms, microfabricated structures (i.e., 'anchors') serve to retain lithographically defined layouts after undercut release.<sup>[7,11–13]</sup> Such strategies play critically important roles in the overall process. The details depend on the device architectures and constituent materials, as described separately in the different sections of this review.

Procedures of casting and curing elastomers such as poly(dimethylsiloxane) (PDMS) provide facile routes to stamps with features of relief whose dimensions can range from millimeters to nanometers.<sup>[14,15]</sup> The low modulus and low surface energy of PDMS allow soft, conformal contact against inks in which nonspecific van der Waals interactions typically dominate the adhesion.<sup>[6,7,9]</sup> Peeling back the stamp retrieves the ink elements by controlled microfracture at their anchoring points.<sup>[16]</sup> Contact against a target substrate followed by removal of the stamp delivers these elements to desired locations, thereby completing the process. High yields rely critically on methods that allow the adhesion strength to the stamp to be reversibly switched between high and low values, for the retrieval and delivery, respectively. One simple technique exploits velocity-dependent energy release rates ( $G = F/w$ ,  $F$ : peeling force,  $w$ : width) between a viscoelastic stamp and a nonviscoelastic ink (Figure 1b).<sup>[9,16]</sup> Here, the critical energy release rate for steady-state crack propagation at the interface between the stamp and ink,  $G_{crit}^{stamp/ink}$ , is governed by the peeling velocity ( $v$ ), while that at the interface between the ink and substrate,  $G_{crit}^{ink/substrate}$ , is independent of  $v$ . At high peeling rates,  $G_{crit}^{stamp/ink}$  becomes greater than  $G_{crit}^{ink/substrate}$ , thereby leading to efficient retrieval of ink elements from the source substrate.<sup>[9,16]</sup> At low peeling rates, the energy release rate by the peeling action reaches the critical energy release rate for the stamp/ink interface first, for high yield delivery to the target substrate.<sup>[9,17,18]</sup> Advanced strategies that further improve the printing efficiency, of particular value for challenging cases (e.g., rough target substrate surfaces), include the use of pulsed laser illumination,<sup>[19,20]</sup> surface relief structures,<sup>[17,18]</sup> or the application of shear strains<sup>[21,22]</sup> (Figure 1c). The transfer yield can be also enhanced by exploiting specific interactions between the inks and the target substrate<sup>[23,24]</sup> or by adding thin layers of adhesive materials such as polyurethane,<sup>[12,25]</sup> polyimide,<sup>[13,26]</sup>



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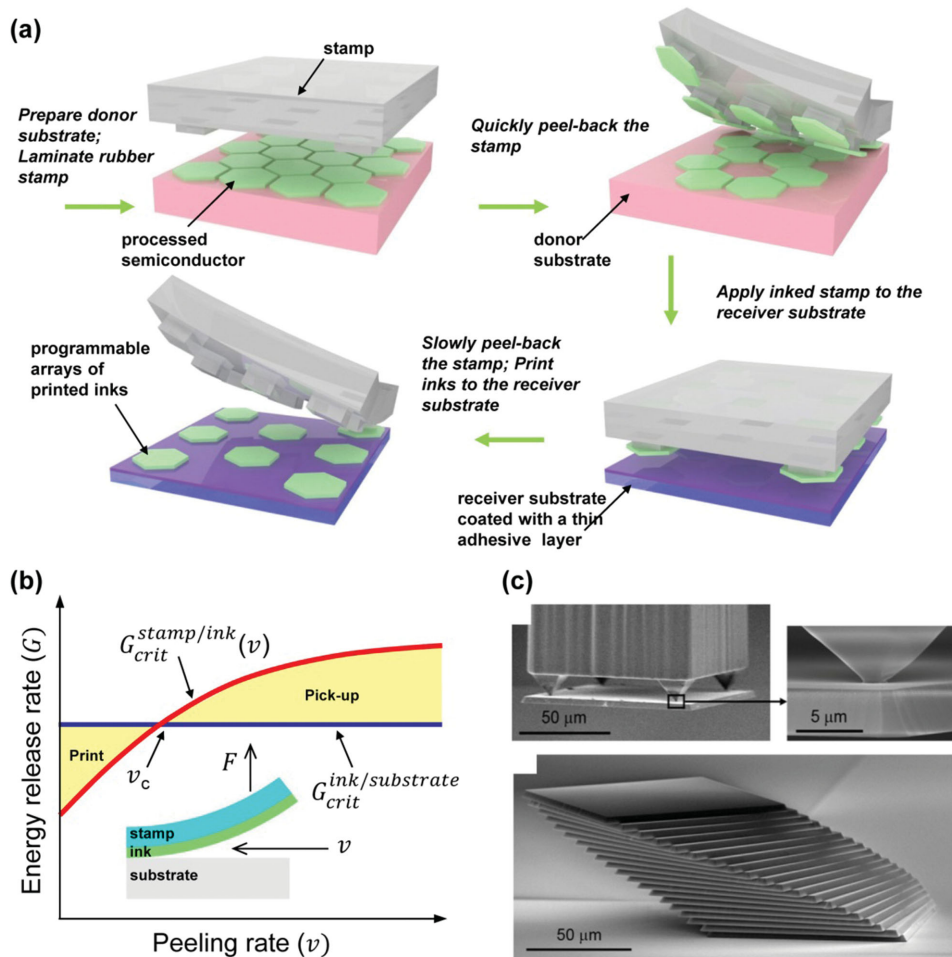
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PDMS,<sup>[12,27]</sup> benzocyclobutene (BCB),<sup>[28,29]</sup> or epoxy resin.<sup>[30,31]</sup> Appropriate use of these approaches can enable yields that approach 100% for nearly any combination of ink, source, and target substrate. In-depth reviews on transfer printing can be found in the literature.<sup>[6,7]</sup> The following sections describe applications in optically active systems.

## 3. Solar Cells

### 3.1. Silicon Solar Cells

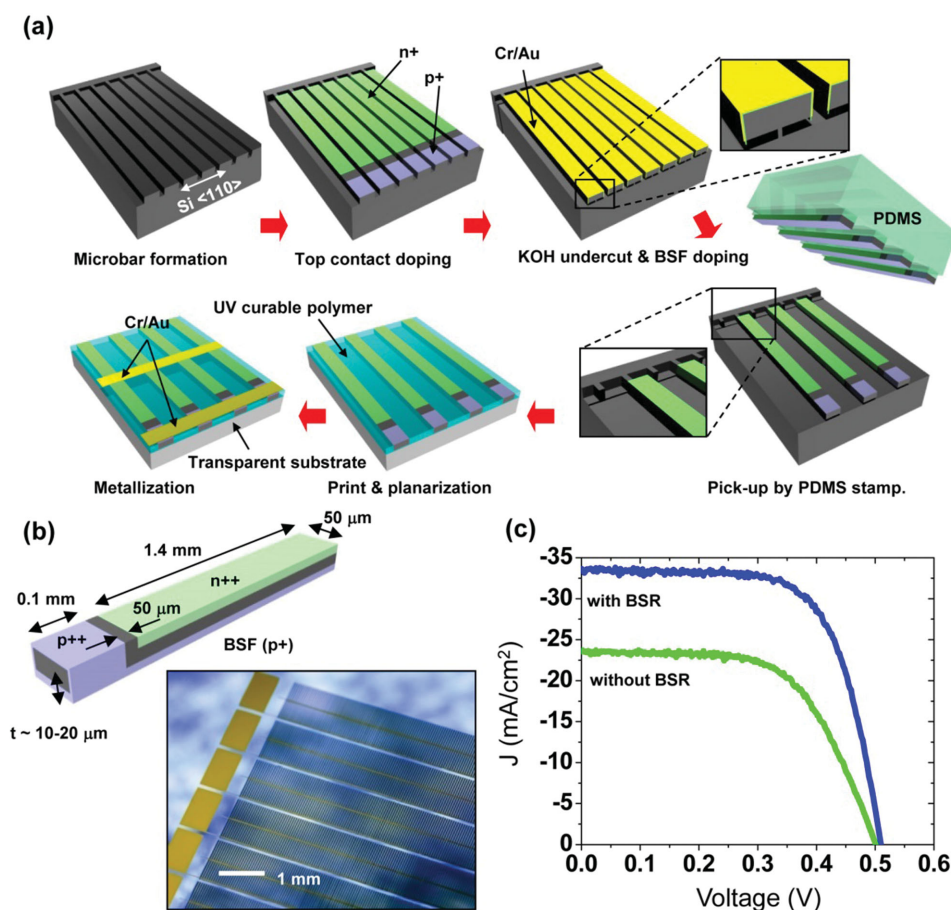
Crystalline silicon remains, by far, the most dominant semiconductor material for photovoltaics (PVs) due to its high natural



**Figure 1.** a) Schematic illustration of the process flow for transfer printing with a soft, viscoelastic stamp. b) Schematic diagram of critical energy release rates for the ink/substrate interface ( $G_{crit}^{ink/substrate}$ ) and for the stamp/ink interface ( $G_{crit}^{stamp/ink}$ ). The intersection of the horizontal line (blue) with the curve (red) represents the critical peel velocity ( $v_c$ ) for the kinetically controlled pick-up and printing processes. Reproduced with permission.<sup>[16]</sup> Copyright 2007, American Chemical Society. The inset illustrates the pick-up of the ink from the donor substrate by the stamp, where  $v$  and  $F$  are steady-state peel rate and peel force, respectively. c) Scanning electron microscope (SEM) images of an elastomeric stamp (top) with micro-tip designs and a stack of silicon platelets (bottom,  $100 \times 100 \times 3 \mu\text{m}^3$ ) printed with small incremental rotations. Reproduced with permission.<sup>[18]</sup> Copyright 2010, The National Academy of Sciences.

abundance, self-passivating surface oxides, high refractive index, excellent transport properties, absorption across the solar spectrum, as well as compatibility with a production infrastructure that can scale to terawatt (TW)-level capacities.<sup>[32–34]</sup> The power conversion efficiencies of the most well developed commercially available silicon solar cells approach theoretical limits.<sup>[35]</sup> Further progress likely emerges from continued reductions in cost, where a value of  $\approx \$0.50 \text{ W}_p^{-1}$  is generally believed to offer parity with conventional sources of grid power.<sup>[32,34,36,37]</sup> Research efforts in silicon photovoltaics tend now to focus on the development of concepts for improved efficiency in materials utilization, cost-effective manufacturing and/or unconventional module architectures. In this regard, ultrathin (compared to conventional cells) forms of single-crystalline silicon might provide routes to high performance, low cost photovoltaics, where carefully optimized designs could reduce the silicon consumption ( $\approx 50\%$  of the total production cost in conventional silicon solar cells<sup>[32,34]</sup>) without decreasing the performance.

While various fabrication methods<sup>[38]</sup> exist for the production of thin-film single-crystalline silicon including epitaxial growth on porous silicon<sup>[39–42]</sup> and kerf-less exfoliation by stress-induced crack formation,<sup>[43–45]</sup> schemes based on anisotropic wet chemical etching<sup>[12,25,46–57]</sup> are of special interest due to their ability to generate a wide range of silicon thicknesses (e.g.,  $\approx 2\text{--}50 \mu\text{m}$ ) from bulk wafers, without the need for additional silicon deposition steps. Suitable layouts yield micro-scale devices (i.e., microcells) in geometries that are compatible with transfer printing, as summarized in **Figure 2**.<sup>[12]</sup> The key idea involves spatially anisotropic etching rates that depend on crystallographic directions in single-crystalline silicon, for wet chemical etchants such as potassium hydroxide (KOH) and tetramethylammonium hydroxide (TMAH).<sup>[58–60]</sup> Fabrication begins with formation of trench-like structures on the surface of a silicon wafer with (111) orientation by photolithography and inductively coupled plasma reactive ion etching (ICP-RIE), such that the long axis of the trench aligns perpendicular to

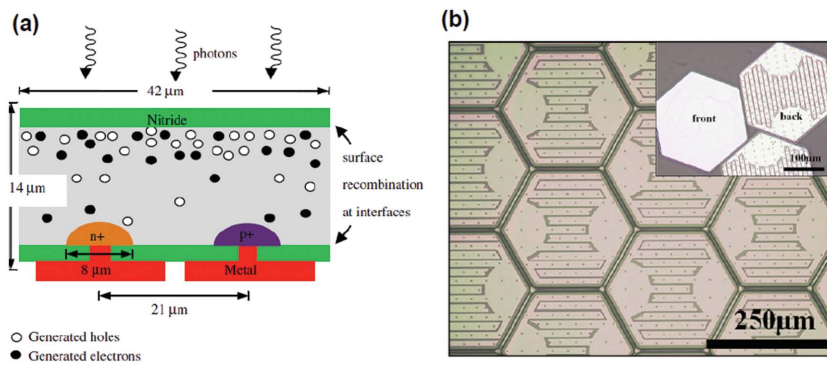


**Figure 2.** a) Schematic illustration of processes for fabricating ultrathin ( $\approx 15 \mu\text{m}$ ) silicon solar microcells from a bulk (111) silicon wafer and assembling them into interconnected modules on a foreign substrate. b) Schematic illustration of a microcell that highlights the dimensions and the doping configurations. The inset shows a photograph of interconnected arrays of silicon microcells printed on glass. c) Representative current density ( $J$ )–voltage ( $V$ ) curves from an individual microcell with thickness of  $15 \mu\text{m}$  under AM1.5D illumination of  $1000 \text{ W/m}^2$ , with and without a white diffuse backside reflector (BSR). Reproduced with permission.<sup>[12]</sup> Copyright 2008, Nature Publishing Group.

the [110] direction of the silicon. In the next step, thermal diffusion of solid-state dopants of boron and phosphorous using lithographically patterned layers of silicon dioxide ( $\text{SiO}_2$ ) and/or silicon nitride ( $\text{Si}_3\text{N}_4$ ) as diffusion barriers define highly doped p- and n-type regions that can serve as electrical contacts to thin film metal electrodes and as the basis for a pn-junction. Subsequently, silicon dioxide ( $\text{SiO}_2$ ) and/or silicon nitride ( $\text{Si}_3\text{N}_4$ ) formed by plasma enhanced chemical vapor deposition (PECVD) protect the top surfaces and sidewalls during undercut etching. Reactive ion etching with masks formed by angled evaporation of metals (e.g., Cr) or photolithography selectively exposes silicon at the bottom surface of the trench, where the preferential etching of silicon in the [110] direction is initiated.<sup>[12,30,51]</sup> After lateral undercut etching in a hot (e.g.,  $100 \text{ }^\circ\text{C}$ ) KOH solution, the exposed bottom surfaces of the microcells are doped by boron to form a back-surface field (BSF) that is electrically connected to heavily doped p-type regions on the top surface. Microcells released along their bottom surfaces remain tethered to the source wafer through narrow ‘anchor’ regions at their ends to maintain the lithographically defined arrangement.<sup>[16,30]</sup> In a related approach, similar anisotropic etching schemes can also produce  $14 \mu\text{m}$ -thick microscale solar

cells with hexagonal shapes and point-contact doping layouts, in which surface passivation by PECVD silicon nitride enables 14.9% efficiency (Figure 3).<sup>[53,57,61]</sup>

All such types of microcells are compatible with transfer printing, as a practical means to manipulate and distribute them onto foreign substrates for module-level assemblies. Specifically, printing allows retrieval of entire collections or selective sets of completed microcells and their delivery onto substrates of interest in a precisely controlled manner at yields approaching 100%.<sup>[12,46,51]</sup> In some examples, a photocurable polymer (e.g., NOA) simultaneously serves as an adhesive layer for the printing and a planarizing medium that fills in the space between the microcells to facilitate the formation of metal interconnects by planar processes of thin film metallization. Repeating these steps with a given source wafer allows distribution of microcells over arbitrarily large areas on low cost substrates, at any desired coverage or layout. Experimental results indicate that efficiencies of microcells with thicknesses of  $\approx 15 \mu\text{m}$  printed on glass substrates lie between  $\approx 6\text{--}8\%$  and  $\approx 10\text{--}13\%$  under AM1.5D illumination ( $1000 \text{ W m}^{-2}$ ) without and with a back-side reflector (BSR), respectively.<sup>[12]</sup> Improved surface passivation and alternative configurations that use

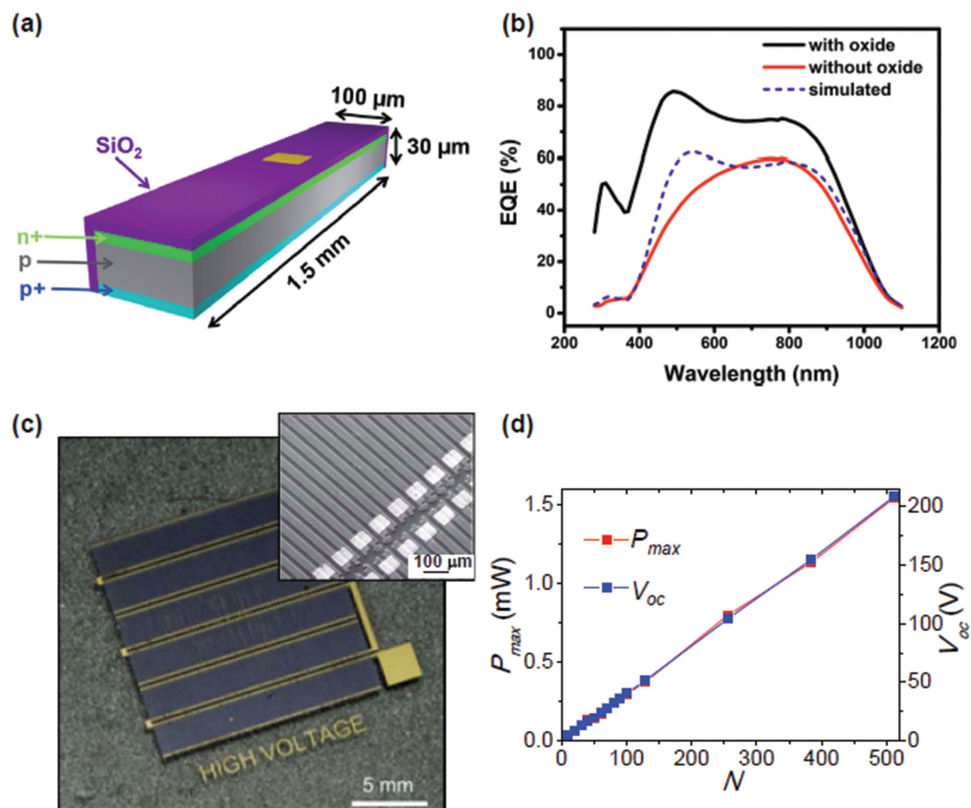


**Figure 3.** a) Representative 2D cross-sectional illustration of 14  $\mu\text{m}$ -thick silicon solar cells, showing dopant profiles and recombination/generation sites. This block repeats 6 times in a 250  $\mu\text{m}$ -wide solar cell. b) Top-view optical micrograph of arrays of such solar cells attached to a (111) bulk silicon wafer. Reproduced with permission.<sup>[53]</sup> Copyright 2011, Elsevier.

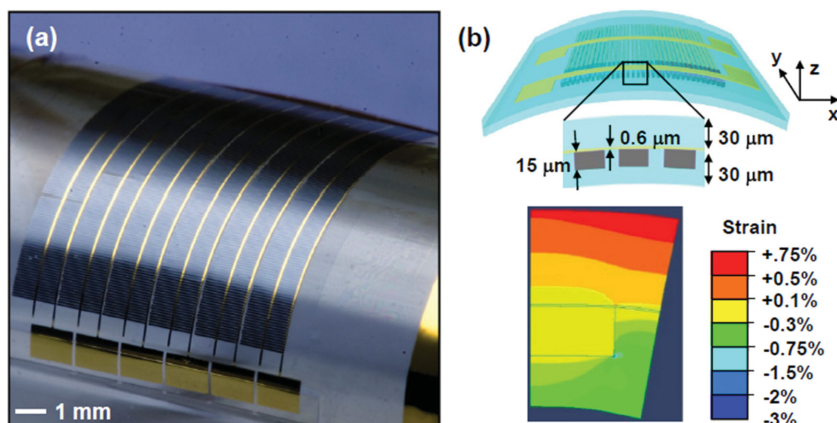
emitter and base contacts on the front and rear-surfaces of the microcells lead to enhanced efficiencies (Figure 4a,b).<sup>[25]</sup> Here, silicon dioxide ( $\text{SiO}_2$ ) grown by wet oxidation serves as an antireflection coating (ARC), surface passivation layer and protective film for the undercut etching, as well as a diffusion barrier for the base boron doping. Resulting microcells with thicknesses of  $\approx 30 \mu\text{m}$  measured on non-reflective surfaces

(e.g., anodized aluminium plates) exhibit one sun efficiencies of  $\approx 12\%$ .<sup>[25]</sup> Printed assemblies allow microcells to be interconnected in ways that can provide desired output characteristics in both current and voltage.<sup>[46,56]</sup> As an example that would be difficult to achieve in conventional designs, compact ( $\approx 0.95 \text{ cm} \times 0.63 \text{ cm}$ ) solar modules with high voltage ( $> \approx 200 \text{ V}$ ) output are possible by series interconnection, where the output voltages scale with the number of interconnected microcells (Figure 4c,d).<sup>[46]</sup>

These schemes enable other options in module design that would be impossible with conventional cells and assembly methods. As an example, mechanically flexible solar modules result from arrays of silicon microcells printed onto sheets of polyethylene terephthalate (PET, thickness  $\approx 50 \mu\text{m}$ ), as in Figure 5.<sup>[12]</sup> Here, bendability follows from placement of the microcells near the neutral mechanical plane (NMP), as defined by the plane where bending-induced compressive and tensile strains cancel. A layer of polymer cast uniformly over the interconnected microcells places the NMP near the most fragile components (i.e., silicon) of the system, thereby effectively isolating them from



**Figure 4.** a) Schematic illustration of a microcell design with improved surface passivation using thermal oxide and a doping configuration that uses emitter and base contacts on the front and rear-surfaces of the microcells. b) External quantum efficiencies (EQEs) for silicon solar microcells with (black) and without (red) the thermal oxide layer. A simulated EQE curve (blue dashed line) for a non-passivated device with enhancements only from an AR coating is also shown. Reproduced with permission.<sup>[25]</sup> Copyright 2013, Royal Society of Chemistry. c) Photograph and SEM (right inset) image of a high-voltage photovoltaic module consisting of interconnected silicon microcells. d) Scaling properties for voltage ( $V_{oc}$ ) and power outputs ( $P_{max}$ ) with numbers of microcells interconnected in series. Reproduced with permission.<sup>[46]</sup> Copyright 2010, Royal Society of Chemistry.



**Figure 5.** a) Photograph of a mechanically bendable module constructed with silicon microcells. b) Color contour plot of calculated bending strains ( $\epsilon_{xx}$ ) through the cross section of a mechanically flexible module, bent along the cell width direction at  $R = 4.9$  mm. The finite element modeling (FEM) calculations use symmetry boundary conditions for evaluating a single unit cell of the system. The black lines delineate the boundaries of the microcell and the metal interconnect line. Reproduced with permission.<sup>[12]</sup> Copyright 2008, Nature Publishing Group.

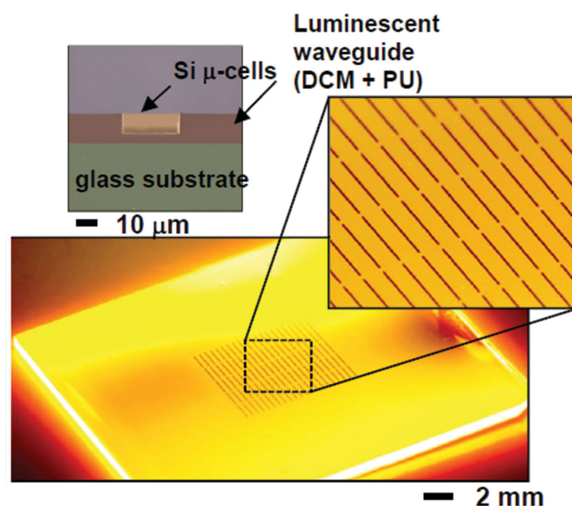
strains due to bending.<sup>[12,62]</sup> As a result, the module performance remains invariant under repetitive bending to radii as small as  $\approx 4.9$  mm and for cycles of up to 200 times or more.<sup>[12]</sup> As another example, the spatial layouts of the microcells can be tailored to yield modules with any selected level of optical transparency, simply by adjusting the cell-to-cell spacings. Such sparse arrays of microcells can also be combined with molded cylindrical lens arrays to form low-profile micro-optic concentrator modules that further minimize the use of the silicon.<sup>[12,61]</sup>

Other unusual forms of concentration are also possible. For example, printed composite assemblies of silicon microcells, waveguides and luminophores allow the construction of an integrated type of luminescent solar concentrator (LSC) module.<sup>[48]</sup> In conventional LSCs, solar cells mount on the edges of a planar, multimode slab waveguide doped with luminophores that absorb incident light and re-emit longer wavelength photons in processes of optical down-shifting.<sup>[63,64]</sup> The emitted photons that satisfy the condition of total internal reflection (TIR) transport to the edges of the slab where they are captured by the edge-mounted cells. Despite attractive features such as the ability to concentrate diffuse light without the need for trackers, the performance of conventional LSCs suffers from optical inefficiencies associated with propagation losses due to absorption, scattering, and/or leaky waveguiding.<sup>[65,66]</sup> LSC designs enabled by transfer printing embed arrays of microcells directly in the waveguiding medium in spatial layouts that match the intrinsic loss characteristics (Figure 6).<sup>[48,67]</sup> In this approach, both direct (through the top surface of microcell) and indirect (through the sidewall and bottom surface of microcell) photon flux contribute to the output power, to enable improved collector efficiencies compared to those of conventional devices. Recent work shows that such LSC systems with CdSe/CdS nanorods as luminophores dispersed in UV-cured poly(lauryl methacrylate) matrices can be engineered to reduce the overlap of emission and absorption spectra (i.e., Stokes shift) and therefore the reabsorption of emitted photons.<sup>[50]</sup> The narrow linewidth of emission from nanorods also allows

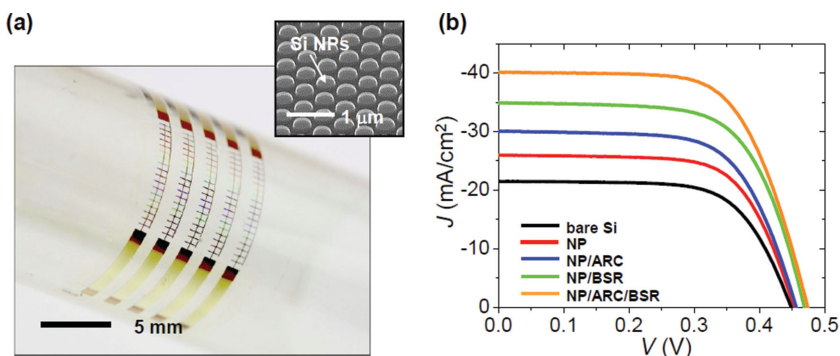
the design of a dielectric Bragg mirror as a front-surface light trapping structure with angle- and wavelength-averaged reflectivity exceeding 99%, which in turn produces concentration factors of nearly 100 even at their comparatively low luminescent quantum yields of  $\approx 35\%$ .<sup>[50]</sup>

These approaches in optical concentration are of interest due to their ability to maximize the utilization of the silicon.<sup>[68]</sup> Another strategy involves use of extremely thin microcells. As the silicon thickness decreases below  $\approx 20$   $\mu\text{m}$ , the absorption of solar radiation decreases significantly due to the indirect bandgap in silicon.<sup>[69–71]</sup> In conventional, wafer-based cells, surface texturization induced by anisotropic wet chemical etching (e.g., in KOH or TMAH) can increase the optical path length in the silicon, thereby enhancing the absorption, although with feature sizes much too large for use in microcells.<sup>[72,73]</sup> Subwavelength

diffractive nanostructures can, however, offer combined effects of antireflection, diffraction, and light trapping<sup>[47,51,74–77]</sup>, suitable for application in microcells. Experimental demonstrations exploit light trapping structures (LTS) based on hexagonally periodic cylindrical nanoposts (e.g., period ( $P$ ) = 500 nm, relief depth ( $RD$ ) = 130 nm, diameter ( $D$ ) = 350 nm) on the front surfaces of silicon microcells ( $\approx 6$   $\mu\text{m}$ ).<sup>[47,49,51]</sup> Optimized choices for  $RD$  and  $D$  enable low reflection losses with maximum coupling of light into diffracted orders for effective trapping. 6- $\mu\text{m}$ -thick microcells with LTS and antireflection coatings (ARCs) provide efficiency enhancements of  $\approx 83\%$  compared to the case of bare silicon.<sup>[47]</sup> Application to microcells with



**Figure 6.** Photograph of a luminescent solar concentrator system with an array of surface-embedded monocrystalline silicon solar cells. The inset shows a magnified image of microcells printed into a photocured layer of polyurethane doped with the luminophore, and a cross-sectional SEM image showing substrate (green), DCM-PU (red), silicon microcell (yellow), and air (blue). Reproduced with permission.<sup>[48]</sup> Copyright 2011, Nature Publishing Group.



**Figure 7.** a) Photograph of large-area, flexible solar modules composed of sparse arrays of interconnected nanostructured silicon microcells printed on a PET substrate, where angle-dependent diffractive colors can be seen. The inset shows a SEM image of hexagonally periodic silicon nanoposts implemented on the emitters of the microcells. b) Representative  $J$ - $V$  curves of individual microcells for various configurations including cells constructed with bare silicon, with arrays of nanoposts (NP), with NP and antireflection coatings (ARC,  $\text{TiO}_2$  60 nm), with NP and back-surface reflectors (BSR), with NP and both ARC and BSR, respectively. The data were collected under the AM1.5G illumination with light is incident on the flat surface of the printed microcells to eliminate the metal shadowing. Reproduced with permission.<sup>[51]</sup> Copyright 2014, American Chemical Society.

thicknesses of just 3  $\mu\text{m}$  with a bilayer ARC ( $\text{Si}_x\text{N}_y/\text{SiO}_2$ ) and a silver backside reflector yield an integrated absorption of  $\approx 77\%$  of AM 1.5D illumination, due to the synergistic effects of LTS and Fabry–Perot cavity resonances.<sup>[49]</sup> The required relief structures can be formed by soft lithographic techniques<sup>[47,49,51]</sup> or, as in recent demonstrations, by block copolymer lithography in which an etch mask of silver islands also provides plasmonic augmentation of the absorption.<sup>[78]</sup> Further advances exploit schemes in which all such processing of photonic nanostructures occurs on the source wafer, before transfer to the module substrate. Examples of such nanostructured silicon microcells with thicknesses of  $\approx 8 \mu\text{m}$  appear in **Figure 7**.<sup>[51]</sup> When printed on transparent substrates, these cells can accept illumination through either the front or rear surfaces. The configuration with nanoposts on the rear surface (i.e., light incident on the flat surface of microcell) eliminates shadowing losses associated with the metal interconnects and also reduces the angular dependence of the performance compared to cells with nanoposts on the front surface.<sup>[51]</sup>

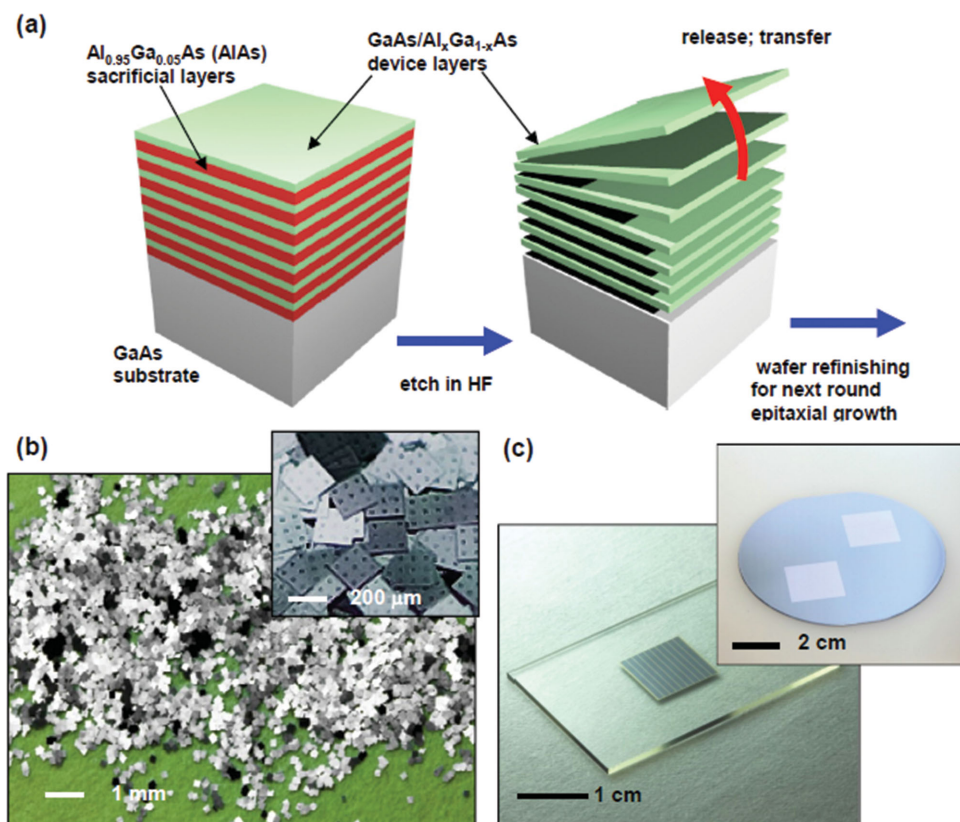
Another surface morphology that offers attractive features for photon management utilizes density-graded nanostructures (so-called ‘black silicon’) for broadband and omnidirectional antireflection and absorption enhancement<sup>[51,52,79–81]</sup>, as demonstrated recently with printed silicon microcells.<sup>[51,52]</sup> Here, metal-assisted wet chemical etching of microcells ( $\approx 5.7 \mu\text{m}$  thickness) with an inverted doping layout forms density-graded rod-like nanocolumns on the front surface.<sup>[51]</sup> Another example uses HBr-based ICP RIE to yield nanocone structures on 30  $\mu\text{m}$ -thick silicon microcells, where randomly dispersed oxide islands act as etch masks.<sup>[52]</sup>

### 3.2. III–V Solar Cells

III–V compound semiconductors serve as the basis for all ultra-high efficiency solar cells due to their wide range of accessible

bandgaps, high electron mobilities, and ability to form monolithic multi-junction devices.<sup>[82,83]</sup> The application of III–V solar cells is largely restricted to space missions, due to the prohibitively high costs associated with device-quality epitaxial materials.<sup>[84]</sup> Advances in processing and growth strategies have the potential, however, to improve the economics of III–V semiconductors in photovoltaics, and allow their competitive use in terrestrial systems. Recent work demonstrates that the concepts of transfer printing can be combined with single or multi-layer epitaxial liftoff (ELO) schemes to yield promising paths to unique classes of ultrahigh concentration modules, where many existing issues for the cost-competitive realization of III–V solar cells can be addressed.<sup>[13,67,85–93]</sup> The following sections summarize advances in materials, fabrication, and transfer printing strategies for III–V compound semiconductor solar cells.

The schemes of epitaxial liftoff, first pioneered by Konagai et al. in 1978,<sup>[94]</sup> enable the release of active epitaxial materials from a growth wafer by selective wet chemical etching of a sacrificial layer.<sup>[95–97]</sup> In the context of photovoltaics, the approach has been widely applied in various GaAs and InP based solar cells because of potential advantages in cost, arising from the substrate reuse and reduced materials consumption.<sup>[22,85,98–105]</sup> Despite progress over the past three decades, however, conventional ELO has not emerged as an economically viable strategy due to many challenges including materials degradation in wafer recycling, low throughput in undercut etching, as well as difficulties in handling the released materials. Many of these issues can be addressed by new ideas, coupled with the methods of transfer printing. A first example appears in **Figure 8** where the starting point is a thick, multilayer stack of device-grade material epitaxially grown on a GaAs wafer by metal organic chemical vapor deposition (MOCVD), with compositions, thicknesses, and dopant characteristics of each layer selected to satisfy specific application requirements.<sup>[13,87,88]</sup> Transfer printing allows the sequential retrieval of individual device layers following release by selective wet chemical etching of embedded sacrificial layers (e.g., AlAs for the case of GaAs). Printing-based delivery and distribution in a step-and-repeat manner to substrates of interest represents a critical step toward large-area, low cost modules. This concept of releasable multilayer assemblies relaxes stringent requirements of wafer reuse (e.g.,  $>100$  times) in conventional, single-layer epitaxial liftoff (ELO)<sup>[84]</sup> and greatly improves the utilization of growth tools by eliminating cycles for load/unload, both of which, taken together, contribute to a significant fraction of the overall fabrication cost of III–V solar cells. Early demonstrations involved ‘n-on-p’ type GaAs single-junction solar cells epitaxially grown in triple-stack assemblies with zinc and silicon as the p- and n-type dopants, respectively.<sup>[13]</sup> Arrays of microscale ( $\approx 500 \times 500 \mu\text{m}^2$ ) solar cells from each device stack follow from delineation of the cell layout by wet chemical etching and



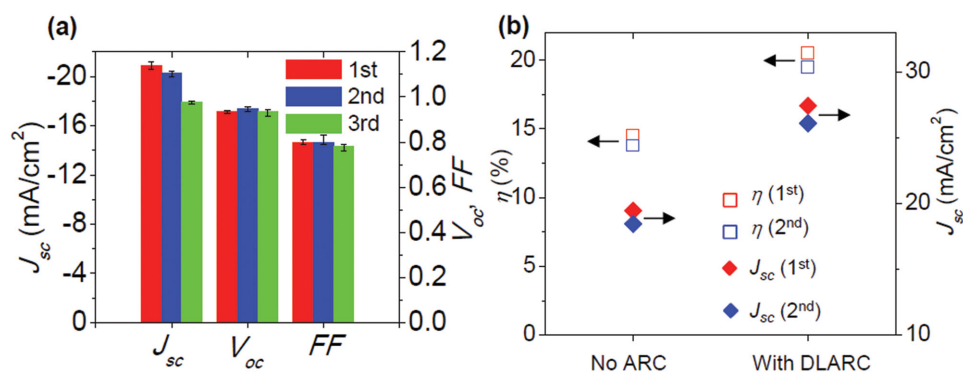
**Figure 8.** a) Schematic illustration of a multilayer stack of GaAs/AIAs and schemes for release through selective etching of the AIAs. b) Photograph of a large collection of GaAs solar cells formed by release from a three-layer stack, and then solution casting onto another substrate. The inset shows a high magnification view. c) Photograph of a GaAs solar cell with a size of  $\approx 10 \times 10 \text{ mm}^2$  printed on a glass substrate with no ARC. The inset shows a photograph of GaAs solar cells with sizes of  $\approx 25.4 \times 25.4 \text{ mm}^2$  printed on a Si substrate. Reproduced with permission.<sup>[13]</sup> Copyright 2010, Nature Publishing Group.

formation of coplanar p- and n-type ohmic contacts by metal evaporation and liftoff processes. Unlike silicon solar cells with ‘homogeneous’ (i.e., silicon) anchor designs as described in the previous section, polymeric (e.g., photoresist) ‘heterogeneous’ anchor structures hold the released devices in the array configuration after complete undercut etching. The projected one-sun (AM1.5D,  $1000 \text{ W m}^{-2}$ ) efficiencies of the multilayer-grown GaAs solar cells based on the experimentally measured external quantum efficiency (EQE) and a double layer antireflection coating (e.g.,  $\text{MgF}_2/\text{ZnS}$ ) are 20.5% and 19.5%, respectively, for devices formed with material from the top and middle layers of the stacks (Figure 9). Recent, alternative epitaxial designs eliminate mobile dopants, such as Zn, in the multilayer growth.<sup>[87,88]</sup> For example, using carbon instead of Zn as a p-type dopant in a ‘p-on-n’ configuration minimizes such effects.<sup>[87]</sup> Further improvements in Zn-doped triple-stack assemblies are possible by exploiting  $\text{Ga}_{0.50}\text{In}_{0.50}\text{P}$  as a window and BSF layers in a p-on-n device configuration.<sup>[88]</sup>

These thin film ( $< \approx 5 \text{ }\mu\text{m}$ ) III–V materials can yield high performance solar cells with attractive options in integration and module designs that cannot be realized in other ways, with significant benefits in cost and performance. As with silicon microcells, output characteristics of modules comprising printed arrays of GaAs microcells can be configured to meet the requirements of specific applications by adjusting

the layouts of parallel- or series-connected microcells.<sup>[13,67,85]</sup> Mechanically bendable or stretchable modules are also possible by heterogeneously integrating collections of microcells onto thin sheets of plastic or slabs of microstructured rubber, respectively.<sup>[13,85,86,88]</sup> As in the case of silicon, NMP designs reduce the bending induced strains in the GaAs in flexible modules (Figure 10a).<sup>[13,85,86]</sup> Systems that can accommodate not only bending but also stretching and twisting, with high areal coverage of microcells (e.g.,  $\approx 70\%$ ) are possible by use of structured elastomeric substrates that have isolated raised regions separated by recessed trenches (Figure 10b).<sup>[85]</sup> Here, arrays of single-junction GaAs microcells ( $\approx 760 \times 760 \text{ }\mu\text{m}^2$ ) interconnected by flexible, ribbon-type structures reside on the raised regions. At an overall strain of  $\approx 20\%$ , the trench regions ( $\approx 156 \text{ }\mu\text{m}$  wide) experience large extension ( $\approx 123\%$ ), thereby absorbing most of the externally applied strains, while the islands ( $\approx 800 \times 800 \text{ }\mu\text{m}^2$ ) barely deform ( $\approx 0.4\%$ ).<sup>[85]</sup> An enhanced version of this type of structure incorporates rectangular notches in the central regions of the edges of the islands to impart both strain-isolation and strain-limiting behaviors.<sup>[86]</sup> Demonstrations with 19%-efficiency GaInP/GaAs dual junction solar microcells at a high ( $\approx 67\%$ ) areal coverage illustrate that the notches effectively increase the length of interconnect lines, which in turn reduces the imposed strain and hence increases the overall stretchability compared to cases without notches.<sup>[86]</sup>

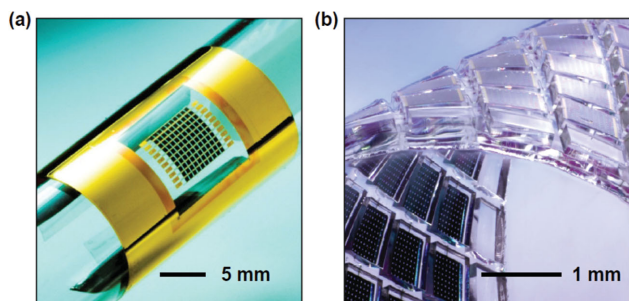




**Figure 9.** a) Short-circuit current density ( $J_{sc}$ ), fill factor ( $FF$ ), and open circuit voltage ( $V_{oc}$ ) of devices derived from the first, second, and third layers in multilayer stacks grown on a source wafer. b) Projected efficiencies ( $\eta$ ) and  $J_{sc}$  values without ARC and with double-layer ARC (DLARC; MgF<sub>2</sub>/ZnS) for devices formed using materials from the first and second layers. Reproduced with permission.<sup>[13]</sup> Copyright 2010, Nature Publishing Group.

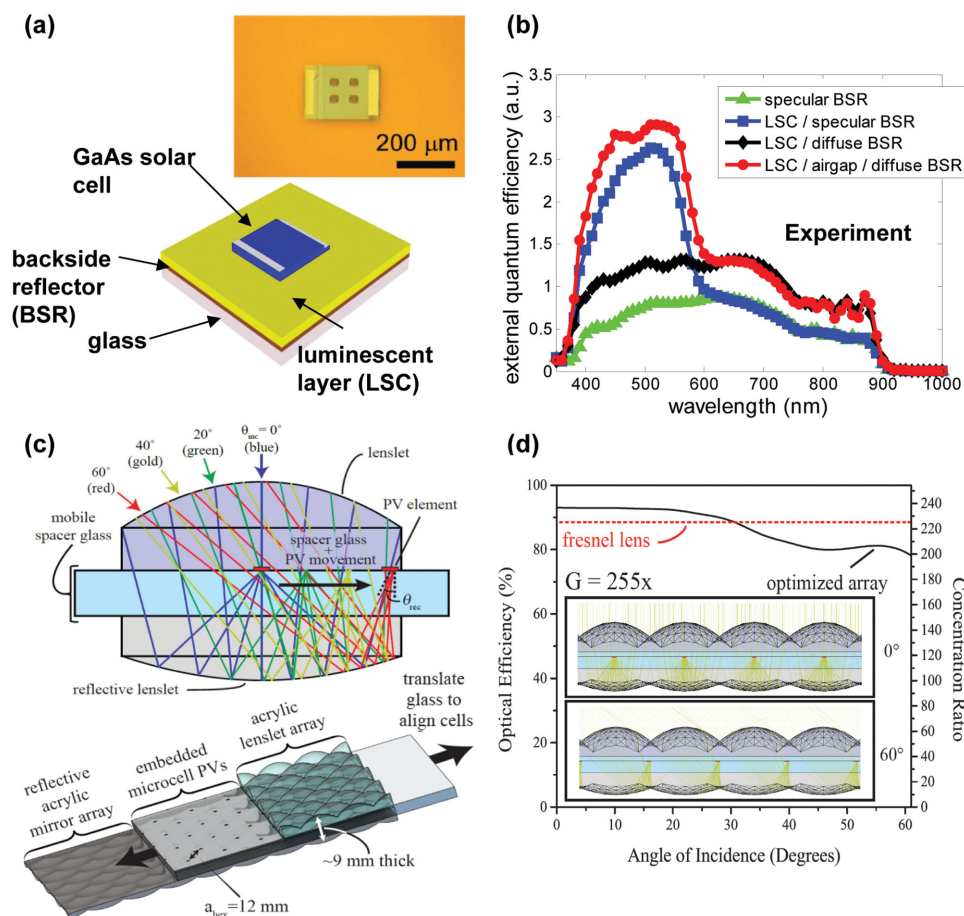
This layout also offers a strain-limiting capability, in which spaces remain for interconnects even at levels of deformation (e.g., inward bending) that cause physical contact of the top-surface edges of the islands.

As with silicon, transfer printed GaAs microcells can be used in unusual concentrator designs. In one example, integration of bifacial GaAs microcells ( $240 \times 200 \mu\text{m}^2$ ) into LSCs improves the power output (Figure 11a,b).<sup>[67]</sup> Here, additional enhancements follow from optimized use of reflective elements placed underneath the luminescent medium. In particular, a diffuse BSR, placed with a small air gap between the LSC increases the short circuit current density ( $J_{sc}$ ) by nearly a factor of two, due to enhanced photon capture from broadband scattering, without any reduction in the waveguiding effect.<sup>[67]</sup> In another case, wide-angle planar microtracking concentrator modules capable of maintaining >200-fold increase in power output (compared to an unconcentrated cell) for incidence angles ranging  $\pm 60^\circ$  follow from embedding GaAs microcells between a pair of plastic lenslet arrays (Figure 11c,d).<sup>[91]</sup> A folded optical path configuration that combines refraction from a convex front surface with reflection from a concave rear surface virtually eliminates the Petzval curvature and produces an intermediate focal plane between the two, where a small (<1 cm) lateral translation at fixed latitude tilt enables full-day tracking with >200 $\times$  flux concentration ratio.



**Figure 10.** a) Photograph of a solar module consisting of a  $10 \times 10$  array of GaAs microcells on a PET substrate. Reproduced with permission.<sup>[13]</sup> Copyright 2010, Nature Publishing Group. b) Photograph of stretchable GaAs solar module formed on a structured PDMS substrate. Reproduced with permission.<sup>[85]</sup> Copyright 2011, John Wiley & Sons.

Single-junction solar cells suffer from inefficiencies related to thermalization of absorbed photons with energies higher than the bandgap, as well as inability to capture photons with energies lower than the bandgap.<sup>[106,107]</sup> These and other effects are captured in the Shockley–Queisser limit on performance of single-junction cells.<sup>[106]</sup> Monolithically grown multijunction solar cells represent the most highly explored route to efficiencies that bypass these limits.<sup>[82,83,89,108]</sup> Stringent requirements on lattice and current matching represent daunting challenges that restrict the application of such cell architectures to a narrow range of materials systems and a relatively small number of junctions.<sup>[82,83,109]</sup> Mechanical stacking of separately grown cells represents an alternative route to multijunction devices, without any intrinsic limit on materials types or numbers of junctions.<sup>[110–114]</sup> Practical difficulties associated with wafer bonding, interface management and others have, however, prevented any commercial use of stacking, in spite of decades of effort in this direction. Transfer printing, combined with high index, dielectric interface layers, offers an attractive alternative to previous attempts. Recent experiments demonstrate four-terminal, quadruple-junction cells formed in this manner and their use in high concentration modules, including on-sun evaluation (Figure 12).<sup>[89]</sup> Here, triple-junction InGaP/GaAs/InGaAsNSb stacks designed with a sacrificial layer for ELO serve as inks for transfer printing onto diffused-junction Ge solar cells to form deterministically assembled quadruple-junction cells, with efficiencies of 43.9% at concentrations exceeding 1000 suns. A key in this architecture is a layer of As<sub>2</sub>Se<sub>3</sub> as an interfacial material between the top III–V and bottom Ge solar cells, that offers high breakdown strength, high thermal conductivity, optical transparency in the infrared wavelength, and refractive index ( $\approx 2.7$ ) similar to the adjacent semiconductors to minimize reflection losses and barriers to thermal transport. Such transfer printed multi-junction microcells have been exploited in pilot-scale commercial concentrator modules with certified efficiencies of 35.5% (Figure 13), where arrays of microcells printed on a ceramic submount substrate integrate with two-stage, low profile optical designs composed of primary silicone-on-glass plano-convex lenses and secondary ball lenses to produce geometrical concentration ratios exceeding 1000.<sup>[115,116]</sup>

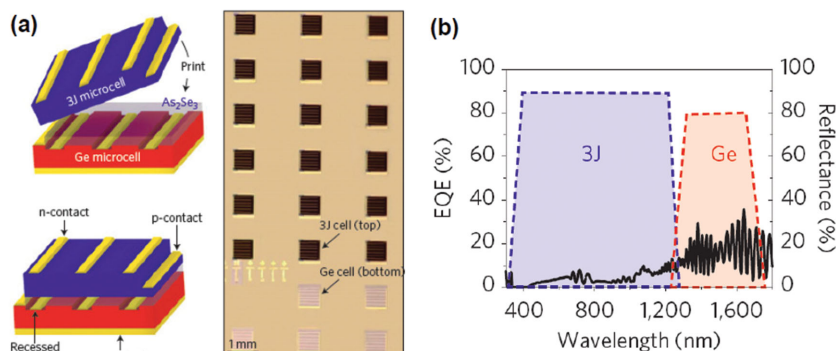


**Figure 11.** a) Schematic illustration of a GaAs microcell transfer printed onto a luminescent solar concentrator (LSC) layer, with a backside reflector (BSR) on the surface of a supporting glass substrate. The upper inset shows an optical micrograph of a GaAs microcell ( $200 \times 200 \mu\text{m}^2$ ) with four holes to facilitate epitaxial liftoff, and metallic ohmic contacts, printed on an LSC substrate. b) Measured EQE spectra of printed GaAs microcell at various LSC substrate configurations including polyurethane (PU)/specular BSR (green triangle), LSC/specular BSR (blue square), LSC/diffuse BSR (black diamond), and LSC/air gap/diffuse BSR (red circle). Reproduced with permission.<sup>[67]</sup> Copyright 2013, John Wiley & Sons. c) Schematic illustration of a microtracking microcell CPV panel. An array of microcells transfer-printed on a central acrylic sheet tracks the sun by sliding laterally between stationary upper and lower acrylic lenslet arrays lubricated by index matching oil. The upper inset shows a ray-tracing schematic illustration of the elimination of Petzval curvature through use of a folded optical path, where the focal plane lies in the middle of a dielectric with refractive upper and reflective lower surfaces. d) Simulated absolute optical efficiency of the array in (c), defined as the fraction of incident optical power in the AM1.5D solar spectrum delivered to the surface of the microcell, where the microcell dimensions are  $0.7 \times 0.7 \text{ mm}^2$ , and the lattice constant of the lenslet array is 12 mm, yielding a nominal geometric gain  $G = 255$ . Reproduced with permission.<sup>[91]</sup> Copyright 2015, Nature Publishing Group.

### 3.3. Organic Solar Cells

For organic or polymeric photovoltaic devices (OPVs), solution-based fabrication methods such as spin-coating or screen printing offer high throughput, low cost, and large-area processibility.<sup>[117–119]</sup> However, construction of multilayer device structures with controllable, well-defined materials interfaces and geometrical patterns is often demanding in such wet deposition processes due to the requirements of solvent orthogonality<sup>[120,121]</sup> and intrinsic immiscibility between dissimilar conjugated polymers.<sup>[122,123]</sup> In this context, transfer printing offers complementary fabrication pathways for multilayered architectures in organic solar cells, for enhanced performance as well as expanded choices of materials combinations, compositions and spatial organizations.<sup>[124–131]</sup> In one example, bilayer

solar cells composed of poly(3-hexyl thiophene) (P3HT):[6,6]-phenyl C61-butyric acid methyl ester (PCBM) formed by transfer printing in ambient conditions offer improved rectifying behavior compared to devices that use blends, due to an increased shunt resistance.<sup>[124]</sup> More recently, experiments show that transfer printing allows facile control over a vertical compositional gradient in P3HT:PCBM active layers to increase the power conversion efficiency by  $\approx 23\%$ .<sup>[127]</sup> Here, a solution of P3HT and PCBM in chlorobenzene spin-cast on a UV/ozone treated silicon wafer yields a layer in which the hydrophilic PCBM resides near the silicon surface. A two-step transfer printing process with the resulting film results in solar cells with P3HT- and PCBM-rich active layers at the interface with  $\text{MoO}_3$  anode and Ca/Al cathode, respectively, to improve the charge collection at the electrodes.<sup>[127]</sup>



**Figure 12.** a) Schematic illustration of a cell from a 3J thin-film stack of InGaP/GaAs/InGaAsNSb derived from epitaxial growth and liftoff and a separate 1J Ge cell, before (upper) and after (lower) assembly by transfer printing. The  $\text{As}_2\text{Se}_3$  layer (light blue) and the recessed metal contact lines on the top of the Ge cell ensure excellent optical, electrical, and thermal properties at the interface. The right inset shows an optical micrograph of an array of 3J/Ge microscale solar cells. b) Schematic illustrations of the EQE and measurement of the reflectance spectra of a 3J/Ge cell. Reproduced with permission.<sup>[89]</sup> Copyright 2014, Nature Publishing Group.

## 4. Light-Emitting Devices

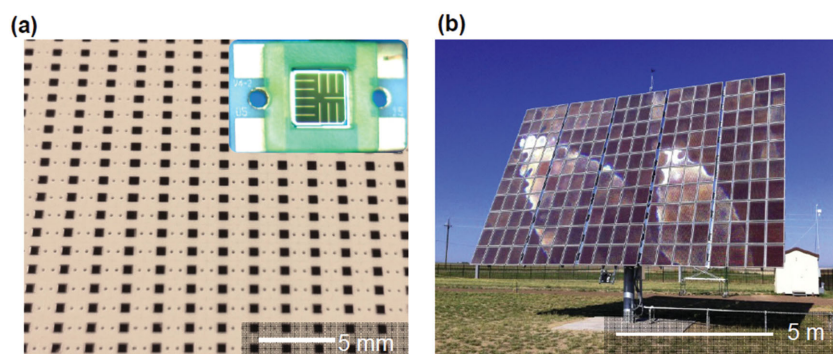
### 4.1. Inorganic Light-Emitting Diodes

Inorganic light-emitting diodes (referred to here as ILEDs to highlight the distinction over organic light emitting diodes, OLEDs) represent a ubiquitous technology with widespread importance in nearly all lighting applications, from residential general illumination, to traffic signals, digital billboards, display backlights, portable light sources and others. The performance characteristics involve a combination of brightness, lifetime, and color purity that make ILEDs attractive compared to nearly any other option, including incandescent light bulbs, fluorescent lamps and OLEDs.<sup>[132,133]</sup> The vast majority of commercial applications of ILEDs rely on millimeter scale dies produced by wafer-dicing, robotic pick-and-place, and wire bonding, thereby imposing fundamental limitations on the device sizes, spatial densities in arrays, form factors, levels of power, and efficiency of materials utilization. Techniques of release and transfer printing, similar conceptually to those described in previous sections, lead to classes of ILEDs and their heterogeneous

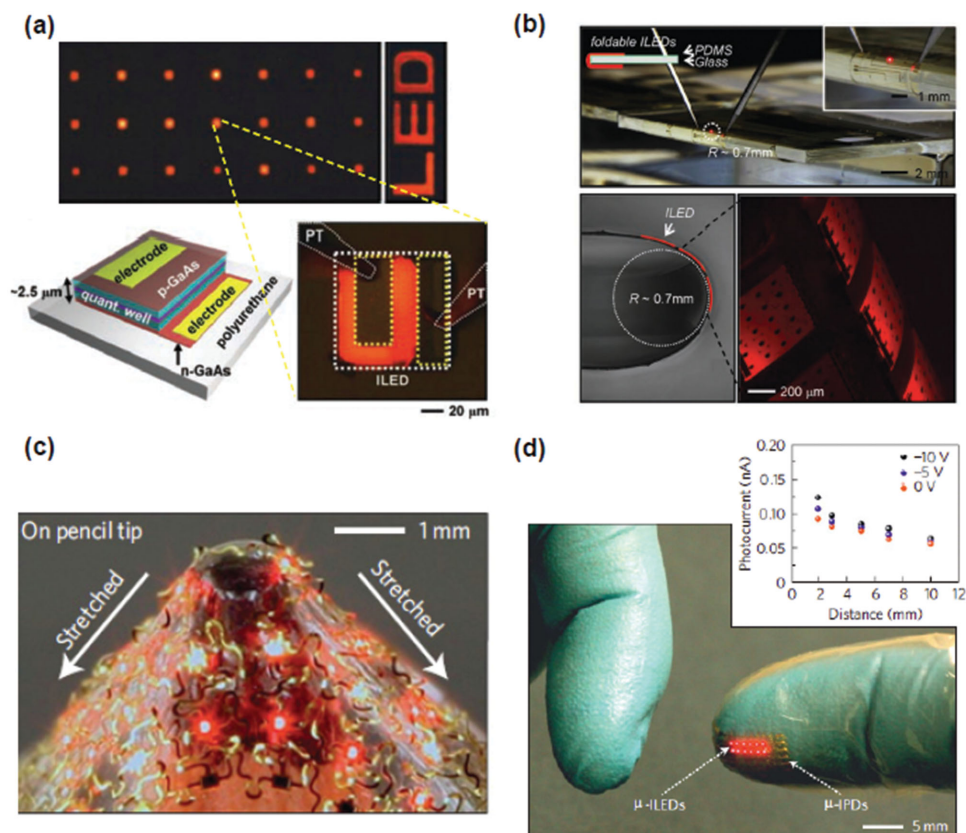
assemblies that circumvent restrictions of conventional systems, with diverse options in devices, ranging from high resolution flexible displays to cellular-scale, biocompatible light sources.<sup>[11,24,27,29,31,134–136]</sup> This section provides an overview of recent advances in this direction with a focus on materials designs and fabrication concepts for microscale ILEDs ( $\mu$ -ILEDs), including foundational studies of the thermal and mechanical properties, as well as circuit- and system-level demonstrations in various unconventional areas of application.

As with the other compound semiconductor devices covered in this review, transfer printing of ILEDs requires specialized designs and processing steps that permit the separation and release of active materials from a growth wafer. The first demonstration of transfer-printed ILEDs involved red-emitting epitaxial materials based on AlInGaP-based quantum wells (i.e.,  $\text{In}_{0.56}\text{Ga}_{0.44}\text{P}$  wells (6 nm)/ $\text{Al}_{0.25}\text{Ga}_{0.25}\text{In}_{0.5}\text{P}$  barriers (6 nm)) (Figure 14a).<sup>[27]</sup> Microscale (e.g.,  $\leq 250 \times 250 \mu\text{m}^2$ ) device layouts with coplanar<sup>[27]</sup> or vertical<sup>[135]</sup> contact configurations follow from photolithography and combinations of dry and wet etching processes. After undercut etching of a sacrificial layer ( $\text{Al}_{0.96}\text{Ga}_{0.04}\text{As}$ ), the isolated device arrays remain tethered to the GaAs source wafer by ‘breakable’ polymeric (e.g. photoresist) anchor structures.<sup>[27,135]</sup> Transfer printing allows selective retrieval of  $\mu$ -ILEDs and their delivery to substrates of interest, sometimes facilitated by use of thin layers of photo- or thermally curable polymer adhesives. As in printed III–V solar cells, the small thicknesses ( $\approx 2.7 \mu\text{m}$ ) of such  $\mu$ -ILEDs allow planar processing to define electrical interconnects for large-scale arrays in lighting elements or addressable displays, where materials ranging from thin films of conventional metals<sup>[27,31,135,137]</sup> to graphene<sup>[134]</sup> serve as electrodes and conducting elements.

Such printing-based assemblies of  $\mu$ -ILEDs provide radically different design options compared to those associated with conventional, large, thick devices that must be packaged individually with wire-bonds and serial, robotic pick and place operations. One of the most enabling examples is in the use of  $\mu$ -ILEDs on mechanically flexible or even stretchable substrates. Such device platforms are potentially useful for building conformal displays and/or lighting systems that can be intimately integrated with soft, curvilinear surfaces such as those of biological tissues. Concepts similar to the ones discussed in the context of solar modules apply to  $\mu$ -ILEDs, where the devices can be placed at the NMP<sup>[12,62,137]</sup> to eliminate strains in the quantum well, and associated changes in the emission wavelength from shifts in the bandgap. Careful studies of flexible AlInGaP  $\mu$ -ILEDs in representative devices reveal that bending-induced tensile and compressive strains in the quantum



**Figure 13.** a) Photograph of transfer-printed multijunction GaAs solar cells on a low cost ceramic sub-mount substrate. The inset shows a magnified view. b)  $64 \text{ m}^2$  single-tracker system installed in the Southwest USA, composed of 200 modules and 132 000 transfer-printed multijunction microcells. Reproduced with permission. Copyright 2015, Sempruis Inc.

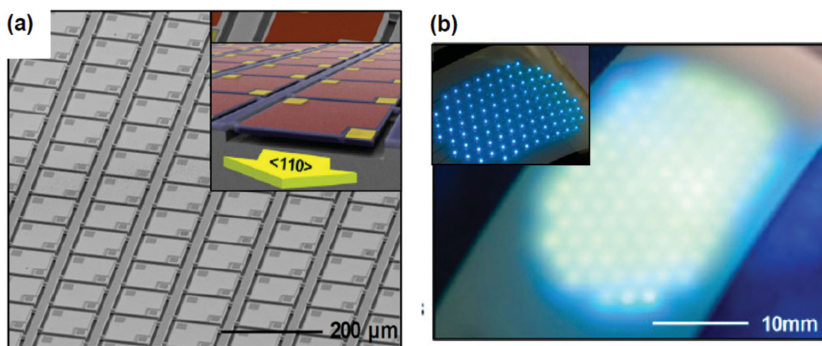


**Figure 14.** a) Optical micrographs of an array of microscale AlGaInP inorganic light emitting diodes ( $\mu$ -ILEDs) (left:  $25 \mu\text{m} \times 25 \mu\text{m}$ , square geometries; right: characters “LED”) in their on state. Schematic illustration (bottom left) of an ILED with integrated ohmic contacts and optical image (bottom right) of an operating device. Reproduced with permission.<sup>[27]</sup> Copyright 2009, The American Association for Advancement of Science. b) Optical images (top) of  $\mu$ -ILEDs ( $100 \times 100 \mu\text{m}^2$ ) in a neutral mechanical plane layout, during operation wrapped around the edge of a slide glass (1 mm thick). The bending radius is  $<0.7$  mm. Cross-sectional optical image (bottom left) of a similar  $\mu$ -ILED system and composite magnified view (bottom right) to show bending in the individual devices. Reproduced with permission.<sup>[137]</sup> Copyright 2013, John Wiley & Sons. c) Photograph of a  $6 \times 6$  array of  $\mu$ -ILEDs, tightly stretched on the sharp tip of a pencil. d) Photograph of a  $4 \times 6$  array of  $\mu$ -ILEDs with serpentine metal bridges, transfer-printed on the fingertip region of a vinyl glove. The inset shows a plot of photocurrent as a function of distance between the sensor and an object (white filter paper) for different reverse biases and different voltages. Reproduced with permission.<sup>[31]</sup> Copyright 2010, Nature Publishing Group.

wells yield red- ( $\Delta\lambda \approx +1.77$  nm) and blue-shifts ( $\Delta\lambda \approx -1.27$  nm) of emission wavelengths, at bending radii of 4.9 and 5.4 mm, respectively.<sup>[137]</sup> With NMP layouts, the systems remain functional even after bending to form sharp folds with bending radii of  $\approx 0.7$  mm (Figure 14b).<sup>[137]</sup>

Conformal integration of  $\mu$ -ILEDs on curvilinear, deformable surfaces such as human skin or internal organs demand constructs that can accommodate more complex deformation modes than simple bending, including high degrees of stretching.<sup>[27,31]</sup> Building on recent advances in flexible electronics, stretchable  $\mu$ -ILEDs can be achieved by dual transfer printing.<sup>[27,31,134,138]</sup> In this process, release of fully-functional  $\mu$ -ILEDs formed on a source wafer and delivery to a temporary glass substrate coated with a trilayer of epoxy (SU-8)-polyimide-poly(methyl methacrylate) (PMMA) allows formation of serpentine-shaped ribbons that serve as both structural bridges and electrical interconnects. Interconnected arrays of  $\mu$ -ILEDs fabricated in this way can be released from the glass by dissolving a sacrificial coating (e.g., PMMA), to allow a second transfer printing operation for integration on a pre-strained sheet of elastomer (e.g., PDMS) through selective bonding at

the locations of the devices. Release of the pre-strain yields non-coplanar layouts in the serpentine metal interconnects through nonlinear buckling physics.<sup>[27,31]</sup> The resulting device platform is mechanically robust, with stable operation over a wide range of deformation modes including uniaxial, biaxial, shear, twisting, and other mixed forms, without any appreciable changes in the performance (Figure 14c).<sup>[31]</sup> Here, the non-coplanar interconnects accommodate the great majority of the deformation, such that the maximum strains in the device components remain well below their linear elastic limit.<sup>[31,134]</sup> Such favorable mechanical attributes of flexible assemblies of  $\mu$ -ILEDs together with biocompatible and water-proof packaging schemes enable their intimate integration with the organs of the human body as well as the creation of unusual classes of optical sensors. An example of the former is in arrays of red  $\mu$ -ILEDs integrated on balloon catheters for highly localized photodynamic drug delivery and activation, phototherapy, or spectroscopic characterization of tissue disorders.<sup>[138]</sup> In another demonstration, an array ( $1 \times 4$ ) of red  $\mu$ -ILEDs mounted on PDMS-coated cotton or nylon threads act as light-emitting sutures for accelerated healing, where a fully encapsulating



**Figure 15.** a) SEM image of a dense array of InGaN  $\mu$ -ILEDs on a Si (111) wafer after anisotropic etching of the near-interfacial region of a Si wafer. The inset shows a schematic illustration of the preferential direction for anisotropic wet chemical etching with KOH. b) Optical images of a fully interconnected array of InGaN  $\mu$ -ILEDs with a laminated film of encapsulated YAG:Ce phosphor islands ( $500 \times 500 \mu\text{m}^2$ ) and a diffuser film. The inset shows the corresponding printed LEDs. Reproduced with permission.<sup>[29]</sup> Copyright 2011, The National Academy of Sciences.

layer of PDMS provides a soft, elastomeric, and biocompatible barrier to the surrounding tissue and biofluids for deep-tissue applications.<sup>[31,138]</sup>

Flexible and stretchable  $\mu$ -ILEDs also find use as light sources in unusual classes of integrated optical sensors. Stretchable arrays of  $\mu$ -ILEDs can be integrated with thin, molded plasmonic crystals as refractive-index microsensors for use in intravenous delivery systems, in which transmission of light from  $\mu$ -ILEDs can be correlated to variations in refractive index of the surrounding fluid.<sup>[31]</sup> In a different example, water-proof  $\mu$ -ILEDs and microscale inorganic photodetectors ( $\mu$ -IPDs) co-integrated onto the fingertip of a glove,<sup>[31]</sup> can be used as an optical proximity sensor (Figure 14d).

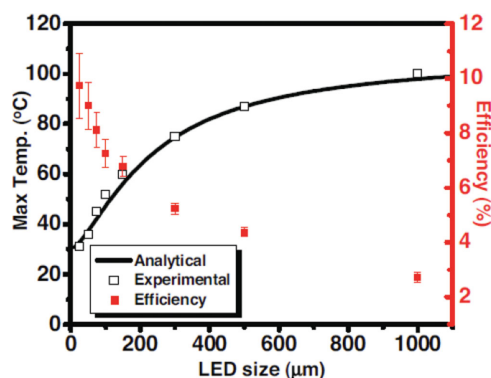
A conceptually similar set of release and transfer printing protocols work well with blue ILEDs. Here, the interest is in devices based on indium gallium nitride (InGaN) for energy-efficient solid-state lighting due to significantly higher luminous efficacies ( $>200 \text{ lm W}^{-1}$ ) than those possible with conventional triphosphor fluorescent lamps (e.g.,  $\approx 90 \text{ lm W}^{-1}$ ).<sup>[139,140]</sup> As with the AlInGaP devices described previously, transfer printing offers an effective route to thin, lightweight, and large-area emissive platforms comprising collections of ultrathin, small InGaN LEDs. Procedures exist for using epitaxial materials grown on silicon or on sapphire.<sup>[11,24,29,141–143]</sup> Silicon substrates are of interest due to their potential for advantages in cost and throughput of materials growth compared to sapphire or silicon carbide (SiC).<sup>[144]</sup> Silicon also offers simple release schemes based on anisotropic wet chemical etching of silicon by KOH or TMAH, similar to those used for silicon solar microcells.<sup>[29,145,146]</sup> In this way, fully-formed arrays of  $\mu$ -ILEDs in freely suspended configurations can be designed in ‘print-ready’ forms (Figure 15a). The large bandgap of GaN ( $\approx 3.4 \text{ eV}$ , 365 nm) allows a remarkably simple process to open the contact regions for the metal interconnection without the need for lithographic patterning. Here, H-line radiation ( $\approx 405 \text{ nm}$ ) that illuminates the system from the backside of a transparent substrate (e.g., glass or plastic) passes through all regions except those shadowed by the metal contacts, to photocure a planarizing polymer over-coat everywhere except

at the contact regions.<sup>[29]</sup> Washing the uncured material away prepares the substrate for photolithographic definition of interconnects, to complete the fabrication. Uniformly emissive lighting modules can be formed from such devices by a laminating a sheet of PDMS with cerium-doped yttrium aluminium garnet phosphors and a diffuser film (Figure 15b).<sup>[29,146]</sup>

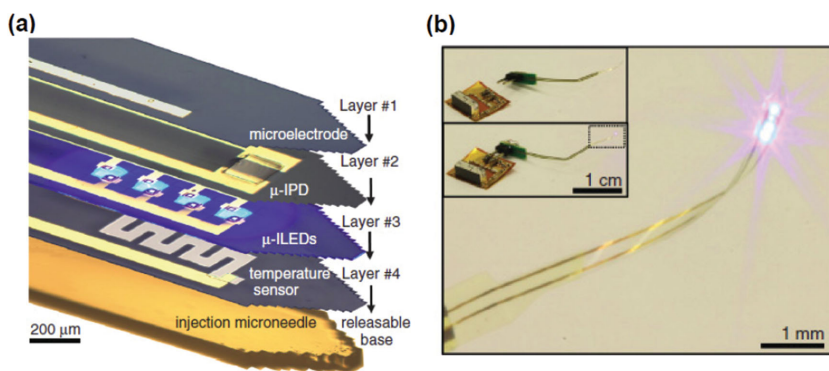
Fabrication routes to printable InGaN  $\mu$ -ILEDs are also available for material grown on sapphire.<sup>[11,24,141–143]</sup> The release operates on fully formed arrays of  $\mu$ -ILEDs, wafer bonded to a temporary silicon substrate using In-Pd alloys for interface adhesion. Conventional laser liftoff then releases the sapphire substrate to yield inverted  $\mu$ -ILEDs on the silicon wafer. Dual transfer printing flips the devices over and selectively delivers them to a target substrate.<sup>[11,24,141]</sup> More

recently, transfer-printed InGaN  $\mu$ -LEDs based on 300-nm-thick nanomembranes have been obtained using conductivity-selective electrochemical etching,<sup>[147]</sup> where heavily doped  $n^+$ -GaN as an anode is selectively removed by an HF-based etchant (HF/glycerol/ethanol). In another approach, 3-nm-thick single-crystalline hexagonal boron nitride (h-BN) serves as a release material that can be laterally fractured by mechanical shear force to allow liftoff of InGaN LED structures grown on sapphire, where AlN is used as a buffer layer to reduce the density of threading dislocations in the overlying InGaN materials.<sup>[148,149]</sup>

In all cases of  $\mu$ -ILEDs, the ultra-small, thin device geometries together with the high thermal conductivity of the metal interconnects yield advantages in thermal management. The rates of passive heat spreading scale favorably with device size, which is enabling for their reliable operation on substrates with low to moderate thermal conductivities such as plastics or biological tissues (Figure 16).<sup>[11,29,150,151]</sup> Moreover, the metal lines serve dual roles as electrical interconnects as well as efficient heat sinks due to their high thermal mass, compared to the  $\mu$ -ILEDs, and thermal conductivities.<sup>[11,29]</sup> Combined theoretical



**Figure 16.** Measured (black symbol) and simulated (black line) maximum temperature as a function of InGaN  $\mu$ -ILED size (lateral dimension) printed on a 50  $\mu\text{m}$ -thick PET substrate, at  $160 \text{ mW mm}^{-2}$ . Red symbols show radiant efficiencies. Reproduced with permission.<sup>[151]</sup> Copyright 2012, John Wiley & Sons.



**Figure 17.** a) A multifunctional, implantable optoelectronic device, in a tilted exploded view layout illustrating various components. The system includes layers for electrophysiological measurement (no. 1: Pt contact pad, microelectrode), optical measurement (no. 2: silicon  $\mu$ -IPD), optical stimulation (no. 3:  $\mu$ -ILED array), and temperature sensing (no. 4: serpentine Pt resistor), all bonded to a releasable structural support for injection (microneedle). b) Integrated system wirelessly powered with RF scavenging. The insets show a connectorized device unplugged (top) and plugged into (bottom) the wireless power system. Reproduced with permission.<sup>[24]</sup> Copyright 2013, The American Association for Advancement of Science.

and experimental studies reveal the effects for  $\mu$ -ILEDs printed on PET substrates.<sup>[11,29,136,152]</sup>

The range of wavelengths and power densities accessible with transfer-printed  $\mu$ -ILEDs, together with their robust, high efficiency operation, enables their use as light source for various biomedical applications.<sup>[11,24,141,146,153]</sup> Recent work demonstrates multi-functional integrated systems for optogenetics,<sup>[24,141]</sup> an emerging field of neuroscience in which light exposure can stimulate or inhibit neural function. Here, InGaN  $\mu$ -LEDs co-integrated with optical, thermal, and electrophysiology sensors and actuators in needle-like multilayer assemblies can be injected into the brain (**Figure 17**). The ultrasmall and thin geometries, low bending rigidities, and high degrees of mechanical flexibility enable a spatially precise, cellular-scale photon delivery vehicle, with reduced tissue damage and minimized inflammation compared to fiber optic alternatives.<sup>[24]</sup> The benefits in thermal management mentioned above, com-

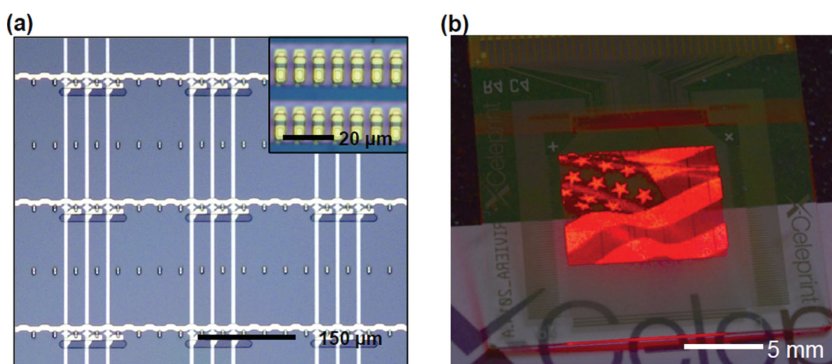
## 4.2. Organic LEDs

With organic or polymeric LEDs (OLEDs), transfer printing provides a means for patterning organic active layers into microscale pixels or constructing multilayered assemblies of different organic materials, where shadow mask deposition or conventional solution-based processes such as photolithography and spin-coating are not applicable due to the limited scalability and pixel resolution, incompatibility of the materials with the processing conditions (e.g., temperature, solvents, developers), or poor control of materials interfaces.<sup>[124,154–158]</sup> In one example, multilayers of organic active materials with aluminium cathodes deposited directly on a poly(urethane-acrylate) stamp pre-treated with a 20 nm-thick layer of fluorinated ethylene propylene (FEP) to reduce the adhesion can be transferred through successive printing operations to form red, green, and blue emitting layers for multi-color pixels in microscale layouts.<sup>[156]</sup>

In a related approach, bilayers of conjugated polymers formed by printing of individual layers with well-controlled organic-organic interfaces serve as active materials for organic LEDs.<sup>[124]</sup>

## 4.3. Quantum Dot LEDs

Inorganic colloidal semiconductor nanocrystals or quantum dots (QDs) based on CdSe, CdS, and ZnS represent promising alternatives to organic luminophores due to narrow emission bandwidths, high quantum efficiencies, and compatibility with solution processing.<sup>[159–167]</sup> Quantum dot based light emitting devices, or QLEDs, can be fabricated by placing a monolayer of quantum dots between electron

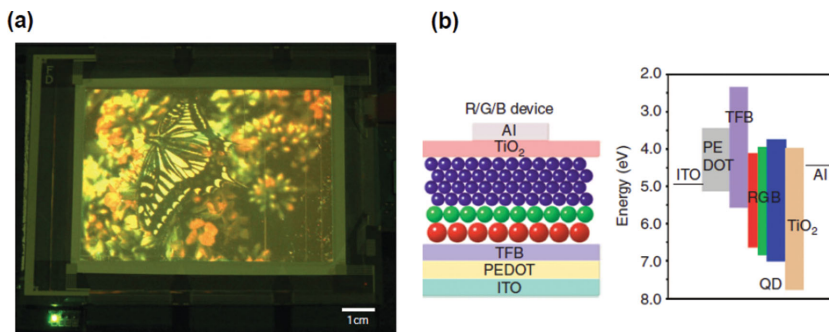


**Figure 18.** a) Optical image of sparse arrays of AlGaInP  $\mu$ -ILEDs with an active area of  $\approx 3 \times 10 \mu\text{m}^2$  printed on a glass substrate, interconnected in a passive matrix array. The inset shows dense arrays of  $\mu$ -ILEDs formed on GaAs growth substrate. b) Passive matrix  $\mu$ -ILED displays of  $120 \times 90$  pixels, printed in a single stamping operation with a pixel size of  $\approx 0.1$  mm. Reproduced with permission. Copyright 2015, X-ceptprint Inc.

and hole-transporting materials with appropriate energy band alignments.<sup>[161–165,167–169]</sup> Conventional solution-based deposition methods such as spin coating,<sup>[163,167,170,171]</sup> drop casting,<sup>[172,173]</sup> phase separation,<sup>[174]</sup> electrodeposition,<sup>[175–177]</sup> Langmuir–Schaefer transfer,<sup>[178,179]</sup> as well as inkjet printing<sup>[180]</sup> or electrohydrodynamic printing<sup>[181,182]</sup> suffer from constraints in film uniformity, interfacial smoothness, and/or materials compatibility between certain solvents and the active materials.<sup>[183,184]</sup> Furthermore, many of these methods have limitations in size-selective patterning of quantum dot domains with geometries essential for pixelated multicolor displays.<sup>[24,168,169,185–188]</sup>

Transfer printing solves many of these challenges. An early example involved patterning of uniform, densely-packed monolayers of QDs as the red-green-blue electroluminescent layers in QLEDs,<sup>[168,185]</sup> in which spin casting of QD solutions onto PDMS stamps constitutes the inking step. Chemically functionalizing the surfaces of the stamps with parylene-C prevents solvent uptake and associated swelling, improves the wetting of the QD suspension, and facilitates their transfer to the target substrate. Similar approaches work well in the formation of white QLEDs,<sup>[186]</sup> using mixed solutions of red, green, and blue CdSe/ZnS quantum dots (24%, 17%, and 59% by weight). Despite these successes, wet-inking processes require modifications to the PDMS stamps, with only limited control over the QD film morphology.

Methods that bypass such difficulties exploit dry transfer printing.<sup>[24,187–189]</sup> In one case, drop casting a solution of QDs onto a donor substrate followed by solvent annealing in an over-saturated atmosphere yields films with excellent long range order. Contact of a PDMS stamp allows selective removal of the QDs and enables their transfer to a target substrate in a completely dry process.<sup>[187]</sup> Sophisticated versions of this scheme allow realization of full-color QLED displays.<sup>[188]</sup> For this demonstration, chemically modifying the donor substrate with an octadecyltrichlorosilane self-assembled monolayer enhances the release of the QDs. Repetitive inking and printing processes in a step-and-repeat sequence yields patterned domains of QD on the receiving substrate. Controlled application of pressure during the printing step ensures that the QDs arrange into densely packed layers with a high degree of ordering both in-plane and out-of-plane.<sup>[189]</sup> The improved morphology and optimized interfaces lead to enhanced power efficiency and brightness compared with spin-cast QLEDs. Full-color, 4-inch active matrix QLED displays with resolution of  $320 \times 240$  pixels follow from sequential transfer printing of multi-color QDs on substrates with hafnium-indium-zinc oxide (HIZO) thin film transistors (**Figure 19a**).<sup>[188]</sup> Further improvements are possible with the use of polyvinylalcohol (PVA) as a sacrificial layer to improve the yields in retrieving the QD monolayers.<sup>[190]</sup> Adhesive-less transfer by use temperature-modulated adhesion and applied shear force together with engineered surface energies of the ‘receiver’ QDs through the ligand exchange yields multilayer assemblies of QD monolayers. Such multilayer stacking



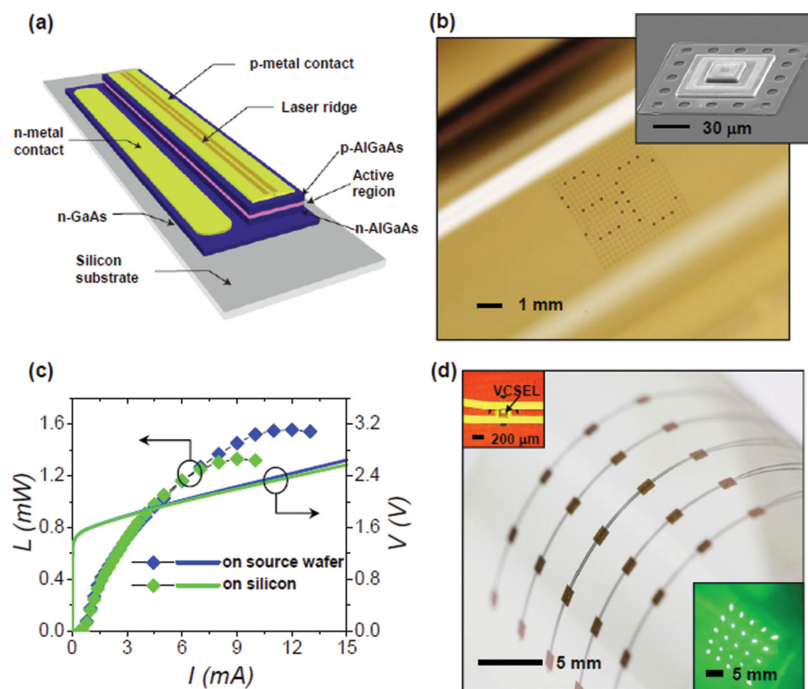
**Figure 19.** a) Photograph of a 4-inch full-color quantum dot display that uses a hafnium-indium-zinc oxide (HIZO) TFT backplane with a  $320 \times 240$  pixel array. Reproduced with permission.<sup>[188]</sup> Copyright 2011, Nature Publishing Group. b) Electroluminescent device with a multistacked QD layer in a R(red)/G(green)/B(blue) sequence. Reproduced with permission.<sup>[190]</sup> Copyright 2013, Nature Publishing Group.

by transfer printing allows deterministic control over the energy-level alignment and energy transfer rate between constituent quantum dot layers, thereby leading to highly balanced white-light electroluminescence (**Figure 19b**).<sup>[190]</sup>

## 5. Lasers

Semiconductor diode lasers based on III–V epitaxial materials are widely used in optical communication systems, laser printers, barcode readers, CD/DVD players, as well as biomedical imaging devices and sensors.<sup>[191–195]</sup> Significant interest exists in the heterogeneous integration of such directional, coherent light emitters with dissimilar classes of materials and devices.<sup>[196–200]</sup> For example, light sources represent essential components for silicon-based integrated photonic circuits for on-chip communications.<sup>[199–201]</sup> Despite clever schemes for inducing light emission directly from the silicon itself<sup>[201–203]</sup> and for hybrid integration by heteroepitaxy,<sup>[204–208]</sup> and wafer bonding,<sup>[207,209,210]</sup> deficiencies in operating efficiencies, constraints associated with materials compatibility and complex processing requirements remain. In this context, transfer printing provides an attractive alternative that allows components (e.g., gain material or reflectors) of laser cavities or entire laser structures to be manipulated directly; it also creates design options that are unavailable to other approaches.<sup>[211–216]</sup>

In one example, epitaxial liftoff and transfer printing enables the integration of GaAs-based edge emitting lasers (EELs) onto silicon substrates for continuous wave (CW) operation (**Figure 20a**).<sup>[211]</sup> Lithographically defined microscale ( $\approx 400 \times 100 \mu\text{m}^2$ ) ‘coupons’ of 5.7  $\mu\text{m}$ -thick GaAs EEL epitaxial layers released by etching of sacrificial layer ( $\text{Al}_{0.95}\text{Ga}_{0.05}\text{As}$ ) can be transferred to a silicon receiver substrate by van der Waals interactions alone, without using any adhesive layers, in which etching (ICP-RIE) forms the mirror facet required for the lasing. The resulting EEL on silicon exhibits single- and multi-transverse mode operation with total optical power of  $>60$  mW, and laser characteristics such as threshold current, total slope efficiency, and small signal modulation bandwidth comparable to those of devices on the growth substrate. The etched facet approach also provides design freedom in the layout of the laser



**Figure 20.** a) Schematic illustration of GaAs-based edge emitting laser (EEL) transfer printed on a silicon substrate. Reproduced with permission.<sup>[211]</sup> Copyright 2012, Nature Publishing Group. b) Photograph of printed micro-VCSELs on a PET substrate with programmable spatial layouts (a logo of 'SC'). The inset shows a SEM image of a micro-VCSEL printed on a glass substrate. c) Representative current ( $I$ )–voltage ( $V$ ) and optical output power ( $L$ )–current ( $I$ ) data collected from 850 nm microVCSELs with an aperture of  $7 \times 7 \mu\text{m}^2$ , on the source GaAs wafer and after the printing on a silicon substrate using a thin ( $<1 \mu\text{m}$ ) adhesive layer. Reproduced with permission.<sup>[214]</sup> Copyright 2014, John Wiley & Sons. d) Photograph of  $5 \times 5$  arrays of interconnected micro-VCSELs with an aperture area of  $22 \times 22 \mu\text{m}^2$  on a thermally engineered flexible substrate, where micro-VCSELs are printed on square-shaped ( $1 \times 1 \text{mm}^2$ ) metal pads (Cr/Ag/Au: 10 nm/3000 nm/30 nm) patterned on a PET substrate. The inset shows the corresponding photograph of VCSEL illumination taken through an IR viewer and a magnified view of an individual micro-VCSEL with metal interconnects. Reproduced with permission.<sup>[230]</sup> Copyright 2015, John Wiley & Sons.

cavity including those, for example, with U-shaped retro-reflectors to double the effective cavity length and make better use of epitaxial materials.<sup>[211]</sup>

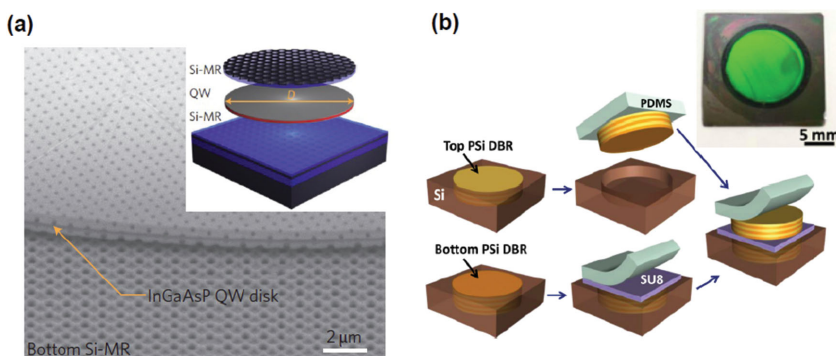
Vertical cavity surface emitting lasers (VCSELs) offer operational characteristics that complement those of EELs, such as surface-emitting beams with circular profiles, low beam divergence, reduced power consumption, as well as facile formation of arrayed layouts.<sup>[194,195,217,218]</sup> Like EELs, heterogeneous integration of VCSELs onto non-native substrates is valuable for a wide range of applications. As such, fabrication methods including flip-chip integration,<sup>[219,220]</sup> wafer-bonding,<sup>[221–223]</sup> fluidic assembly,<sup>[224,225]</sup> and epitaxial liftoff,<sup>[226,227]</sup> and applique<sup>[199,228,229]</sup> offer some value, but none provides practical pathways to manufacturing due to their high cost,<sup>[219,220]</sup> poor control over areal coverages,<sup>[224,225]</sup> tendency to degrade performance,<sup>[199,226,227]</sup> or limitations in substrate materials.<sup>[221,223]</sup> Here again, specialized epitaxial designs and liftoff approaches allow the use of transfer printing as an integration strategy.<sup>[214,230]</sup> Key aspects in the materials design include HF-resistant half-pair low index layers (e.g.  $\text{Al}_{0.40}\text{Ga}_{0.60}\text{As}$ ), coplanar electrical contacts formed within the distributed Bragg reflectors

(DBRs), and double-mesa geometries for individual VCSELs, optimized for the protection of active layers during the undercut etching. The outcome enables defect-free release of ultrathin, microscale VCSELs (micro-VCSELs) from their growth wafers, in ways that are fully compatible with their deterministic assembly by transfer printing on virtually any type of non-native substrate in scalable, precisely controlled layouts (Figure 20b).<sup>[214]</sup> Micro-VCSELs with aperture areas of  $7 \times 7 \mu\text{m}^2$  on silicon substrates exhibit  $IV$  and  $LV$  characteristics comparable to those on the source wafer, thereby confirming the absence of any degradation of the intrinsic materials properties of the DBRs or quantum wells (Figure 20c). Such printable forms of micro-VCSELs provide versatile choices in circuit- or system-level designs. Demonstrations include  $10 \times 10$  arrays of individually addressable micro-VCSELs on thin ( $\approx 50 \mu\text{m}$ ) PET substrates to form mechanically flexible, near-infrared laser displays, in which the electrical and optical characteristics of the devices remain invariant at bending radii of 2.5 mm, and up to 1000 cycles of bending due to the use of NMP layouts.<sup>[214]</sup> This scheme also allows integration of VCSELs with traditionally incompatible materials and devices to create unusual forms of function. For example, micro-VCSELs can serve as light sources on PET substrates co-integrated with silicon-based photodetectors to produce portable and miniaturized optoelectronic sensors for the detection of fluorescence or scattering signals as well as wearable physiological sensors for health monitoring. Thermally engineered composite assemblies of printed micro-VCSELs address core issues in thermal management, with particular relevance

to use of substrates with low thermal conductivities such as plastics.<sup>[230]</sup> Here, a thin metal layer incorporated between the devices and the PET substrate acts as a heat spreading medium to significantly reduce the effective thermal resistance of the system. The enhanced thermal characteristics can reach levels higher than those associated with the source wafer, while preserving critical benefits that follow from the use of mechanically flexible and optically transparent substrates (Figure 20d).<sup>[230]</sup>

Transfer printing can also be utilized for the assembly of discrete components of the laser cavities such as the reflectors and the gain materials to create new types of optically pumped lasers. The primary value of such approaches is in cases where monolithic growth and deposition are inherently limited or impossible due to mismatches in the materials properties and/or processing conditions.<sup>[212,216,231]</sup> Also, such assemblies avoid requirements in electrically connecting the gain medium and reflector, thereby creating choices in materials other than semiconductors for the laser cavity in optically pumped lasers. In one example, InGaAsP-based quantum well heterostructures combine with ultra-compact silicon-based photonic crystal reflectors to realize optically pumped inorganic lasers on silicon





**Figure 21.** a) SEM image of a complete MR-VCSEL, showing the InGaAsP quantum well disk sandwiched between top and bottom Si-MRs. The inset shows a schematic illustration of the multilayer printing process for the formation of an MR-VCSEL (top Si-MR/quantum well/bottom Si-MR). The diameter of the active layer is  $D$ . Reproduced with permission.<sup>[212]</sup> Copyright 2012, Nature Publishing Group. b) Schematic illustration of the process flow for the assembly of porous-silicon-based hybrid microcavities. The inset shows an optical image of completed structure. Reproduced with permission.<sup>[216]</sup> Copyright 2014, American Chemical Society.

(Figure 21a).<sup>[212]</sup> Transfer printing delivers epitaxially grown quantum well structures after their release from InP source substrates, onto SiO<sub>2</sub>-coated silicon membrane reflectors (Si-MR) formed with square arrays of nanoholes fabricated by electron-beam lithography and reactive ion etching. An additional transfer printing step places Si-MRs on top, to form vertically stacked laser resonators. These thin ( $\approx 340$  nm) membrane reflectors replace conventional thick DBRs found in VCSELs, to allow the lasers to be implemented directly on the silicon substrate with significantly reduced overall thickness (e.g.,  $< \approx 2$   $\mu\text{m}$ ). Moreover, the ability to independently control the optical properties and spatial organization of individual reflectors allows multi-wavelength lasing operation on the same substrate.<sup>[212]</sup>

In another example, optically pumped laser structures based on porous-silicon DBRs can be realized by transfer printing (Figure 21b).<sup>[216]</sup> Porous silicon (PSi)-based DBRs produced on highly doped (0.01–0.03  $\Omega\text{cm}$ ) (100) silicon wafers can be constructed with engineered stop-band position and high reflectance. Alternating stacks of high ( $\approx 2.4$ ) and low ( $\approx 1.7$ ) refractive index layers result from controlled electrochemical etching. Thin layers of gain media based on materials such as PbS nanocrystals (dispersed in SU-8) or layers of GaAs can be transfer printed on the bottom PSi DBR; another printing step forms the top DBR. The porosity of the silicon offers additional pathways to control the optical responses via infiltration of other materials. For example, the cavity resonances shift in expected ways by partially or completely introducing Al<sub>2</sub>O<sub>3</sub> into PSi DBRs by atomic layer deposition, in a manner that does not broaden or diminish the strength of the resonant mode. Spatial modulation of the lasing behavior is possible by printing gain media (e.g., GaAs) onto various locations of DBRs that have gradient optical thicknesses and spatial variations in the cavity mode.<sup>[216]</sup>

## 6. Photodetectors

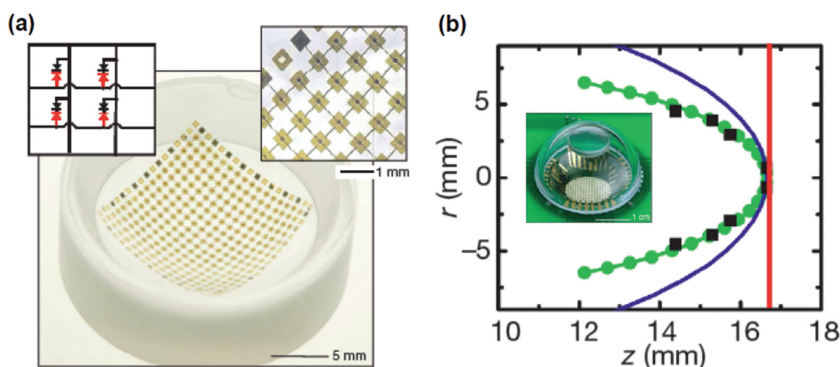
Photodetectors represent one of the most widely utilized components in optoelectronics.<sup>[232,233]</sup> As with the other device

classes discussed in this review, transfer printing offers versatile capabilities in integration, for both conventional and unconventional systems; examples of the latter include imaging arrays on plastic foils or rubber membranes for mechanically bendable modules for bio-integrated applications or bio-inspired designs.<sup>[13,87,135,234–243]</sup> Using processes similar to those described previously, photodetectors can be readily constructed from single-crystalline group IV (e.g., silicon and germanium)<sup>[214,235–238,240–243]</sup> and III–V (e.g., GaAs, InP)<sup>[13,234,239,244]</sup> semiconductors in forms compatible with transfer printing. As an example, eliminating the buried oxide layer from single-crystalline germanium-on-insulator (GOI) substrates after defining lateral p-i-n doping profiles in the top Ge by ion implantation yields 250 nm-thick germanium nanomembrane photodetectors that can be transfer printed

onto PET substrates using a photocurable epoxy as an adhesive.<sup>[236]</sup> Related work shows that a 340 nm-thick, n-type silicon nanomembrane derived from silicon-on-insulator (SOI) substrates can be printed onto PET, where evaporation of p-type pentacene forms a heterojunction that can be used in flexible, semi-transparent photodetectors.<sup>[245]</sup>

Collections of photodiodes can be interconnected to create focal plane arrays for digital imagers<sup>[235,237,238,240,242,243]</sup>. Near-infrared (NIR) imaging systems can be formed on silicon substrates from printed arrays of photodetectors based on bilayers of undoped GaAs and n-type GaAs.<sup>[13]</sup> Deposition of Ti/Au forms metal–semiconductor–metal back-to-back diodes to enable NIR detection, while Schottky diodes made on the n-type GaAs layer serve to block reverse bias current and prevent cross-talk between pixels.<sup>[13]</sup> An even more compelling mode of integration involves assembly of photodetectors on hemispherically curved surfaces to create imaging devices with overall geometries similar to those commonly observed in biological systems.<sup>[235,237,238,240,242,243]</sup> Of historical interest in digital imaging are systems that exploit photodetectors on concave surfaces, in configurations similar to those of mammalian eyes, due to the ability of this geometry to match Petzval image surfaces associated with simple lenses.<sup>[246,247]</sup> Traditional optoelectronics technologies cannot reproduce this type of layout simply because all standard growth, deposition and fabrication processes exist only for use on planar surfaces of semiconductor wafers or glass substrates. Recent work demonstrates schemes based on transfer printing that can overcome such restrictions and realize high performance hemispherical imaging systems.<sup>[235]</sup>

The fabrication begins with transfer printing to form single-crystalline silicon photodiodes and blocking diodes using material derived from SOI wafers. Narrow metal interconnect lines encapsulated with thin polymer films in an NMP layout and configured to allow out-of-plane bending yields a layout that can be mechanically compressed to strains of several tens of percent without inducing fracture in the diodes. Geometric transformation of this originally planar array occurs by



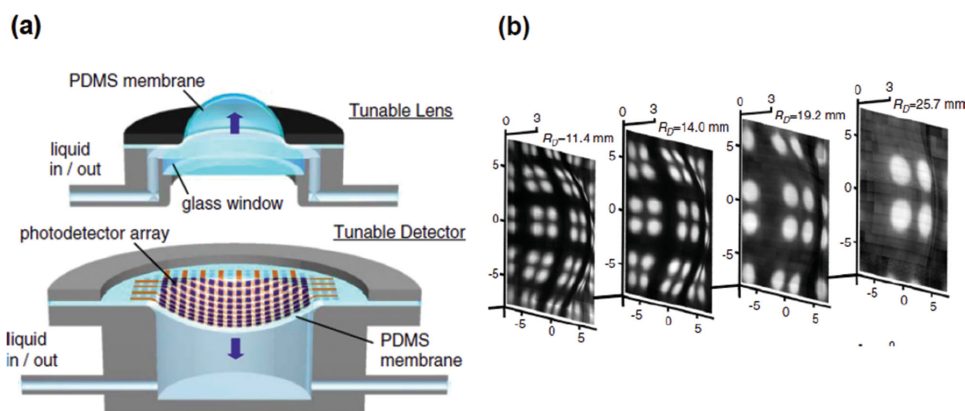
**Figure 22.** a) Photograph of an array of photodetectors (PDs) and blocking diodes (BDs) integrated on a hemispherical glass substrate (main frame), optical micrograph of a part of the array (upper right inset) and circuit diagram showing the BDs (black), PDs (red) and electrode crossovers (arcs) in a  $2 \times 2$  section of the system (upper left inset). b) Ray tracing predictions of the curvature of the optimal focal surface (green circles, calculated focal points; green curve, parabolic fit), the detector surface of the hemispherical camera (blue curve), and a planar camera (red curve). The inset shows a photographic image of the electronic eye camera after integration with a transparent hemispherical cap with a simple, single-component imaging lens. Reproduced with permission.<sup>[235]</sup> Copyright 2008, Nature Publishing Group.

use of a thin elastomeric membrane cast into the shape of a hemispherical shell and then elastically deformed into a planar ‘drumhead’ by radial stretching. Transfer printing the entire interconnected array to the surface of the membrane and then releasing the radial tension allows the elastomer to return to its original shape, compressing and carrying the array with it (Figure 22a).<sup>[235]</sup> The compressive strains associated with this transformation cause the flexible interconnects to adopt out-of-plane arc-shaped structures.

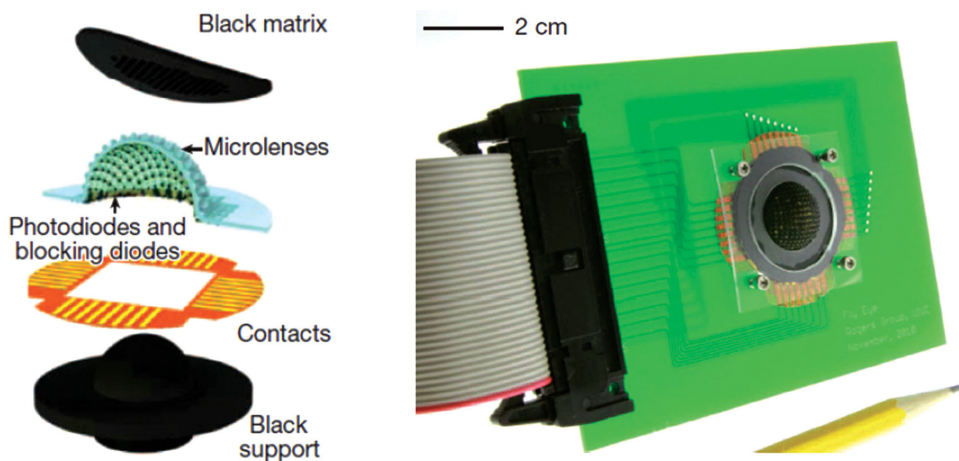
Forming a camera with this array involves bonding it to a solid hemispherical cavity machined into a glass substrate, integrating a plano-convex lens at the appropriate distance, and inserting the entire assembly into a hole in a printed circuit board that supports electrical connections to a separate data

acquisition system. Demonstrated “electronic eye” cameras of this type have approximately the same overall shape and size of the human eye.<sup>[235]</sup> As expected, the match between the curvature of the photodetector array and the Petzval surface improves the field of view and illumination uniformity, while reducing aberrations (Figure 22b). Many advanced versions of this type of system have been explored,<sup>[237,238,240,242,243]</sup> including recent examples by Sony in high resolution imagers.<sup>[248]</sup> The precise shape of the Petzval surface is better approximated by a paraboloid than a hemisphere; the same fabrication sequence, with appropriately designed elastomer membranes, can realize imagers with such shapes.<sup>[237]</sup> Multi-device tiles and extended non-coplanar interconnects can improve the fill factor and deformability.<sup>[238]</sup> The addition of hydraulic actuation schemes allows dynamic adjustment of the curvature, to enable use of imaging lenses with adjustable zoom capabilities (Figure 23).<sup>[240]</sup>

In the most sophisticated and most recent work in this area of bio-inspired design, extensions of the concepts described above enable digital cameras that mimic the architectures found in compound eyes of arthropods (Figure 24).<sup>[242]</sup> Here, an array of convex microlenses and cylindrical supporting posts molded on a stretchable base membrane bonds to a geometrically matched array of interconnected silicon photodiodes and blocking diodes, separately fabricated using transfer printing. Each photodiode lies at the focal plane of a corresponding microlens, to form artificial ommatidia.<sup>[242]</sup> Hydraulic actuation then deterministically transforms the planar layout of these bonded arrays into a full hemispherical shape, in a manner such that the interconnects and the base membrane accommodate nearly all of the strains. The resulting cameras provide two powerful characteristics that



**Figure 23.** a) Schematic illustration of an electronic eye camera with adjustable zoom, including the tunable lens (upper) and tunable detector (lower) modules. The lens consists of a fluid-filled gap between a thin PDMS membrane and a glass window, while the tunable detector consists of an array of interconnected silicon PDs and BDs mounted on a thin PDMS membrane. b) Images acquired by a complete camera system, collected at distances from the lens ( $z$ ) of 16, 24, 38, and 55 mm with corresponding radii of curvature of the lens surface ( $R_L$ ) of 4.9, 6.1, 7.3, and 11.5 mm. The radii of curvature ( $R_D$ ) of the detector surface, set to match the computed Petzval surface shape, were 11.4, 14.0, 19.2, and 25.7 mm. Reproduced with permission.<sup>[240]</sup> Copyright 2011, The National Academy of Sciences.



**Figure 24.** a) Exploded-view illustration of the components of a digital camera that takes the form of a hemispherical, apposition compound eye, including a perforated sheet of black silicone, hemispherical array of microlenses and PDs/BDs diodes, thin film contacts for external interconnects, and hemispherical supporting substrate of black silicone. b) Photograph of a completed camera mounted on a printed circuit board as an interface to external control electronics. Reproduced with permission.<sup>[242]</sup> Copyright 2013, Nature Publishing Group.

are unachievable by planar detector technologies (1) exceptionally wide-angle fields of view (as large as  $\approx 160^\circ$ ) without off-axis aberrations and (2) nearly infinite depth of field due to the short focal lengths of the microlenses and the nature of image formation. These features enable simultaneous imaging of multiple objects at widely different angular positions and distances, with high acuity to motion.<sup>[242]</sup>

## 7. Conclusions

The results summarized here illustrate advances in the development of optoelectronic devices and their circuit/system-level implementation enabled by elastomeric-stamp-mediated transfer printing. They provide practical integration pathways to a broad spectrum of materials that can be co-integrated regardless of conditions for their growth, deposition, or patterning, and thereby maximize their performance and cost-effectiveness or impart novel hybrid functionalities and versatile capabilities not previously possible within established protocols. Some of the most appealing aspects include mechanically flexible and stretchable designs that allow high performance optoelectronic components derived from wafer-based inorganic semiconductors to be intimately integrated on curvilinear, deformable surfaces including skins and internal organs without compromising intrinsic materials properties and performance characteristics. The efforts covered in this review have yielded a wide range of results in device- and circuit-level deployment of such materials together with advanced fundamental understanding over many underlying scientific and engineering principles in materials science, device physics, surface chemistry, solid mechanics, and thermal transport. There exist many additional opportunities for basic and applied research in this field, for the development of novel processing techniques, device architectures, and system configurations, that will even further enhance application possibilities of both existing and emerging materials.

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